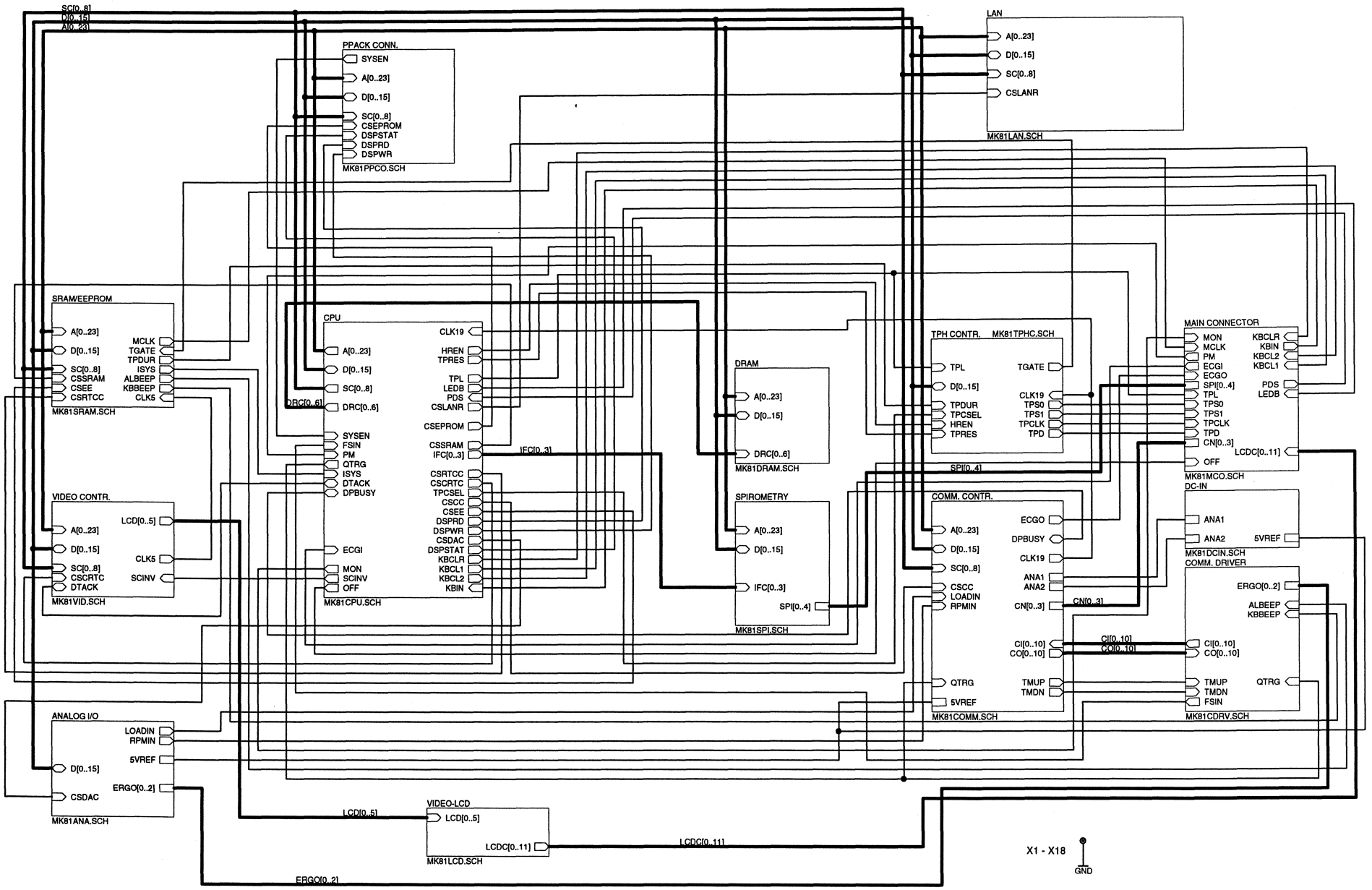
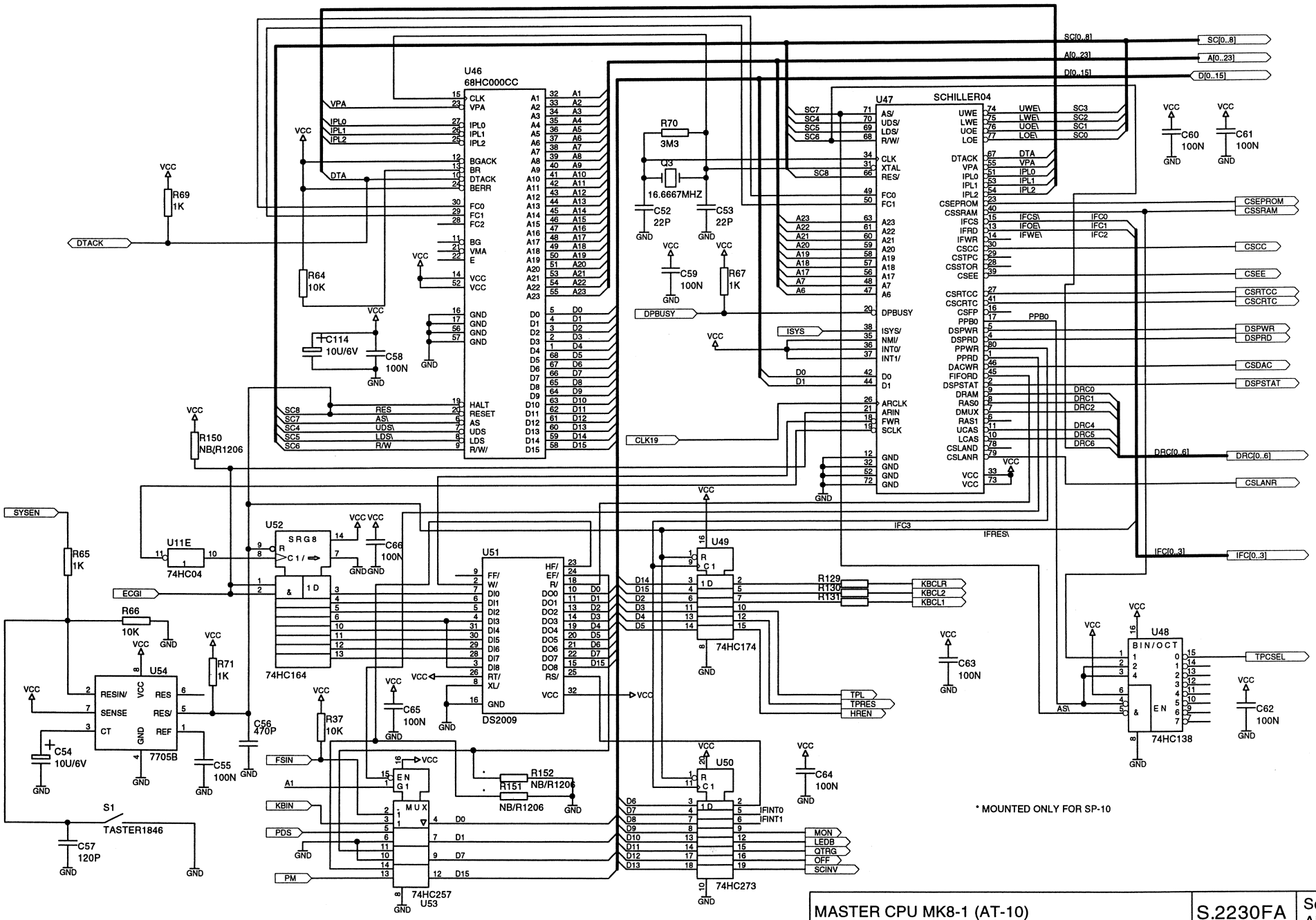
AT-10 Circuit Diagrams and Board Layouts

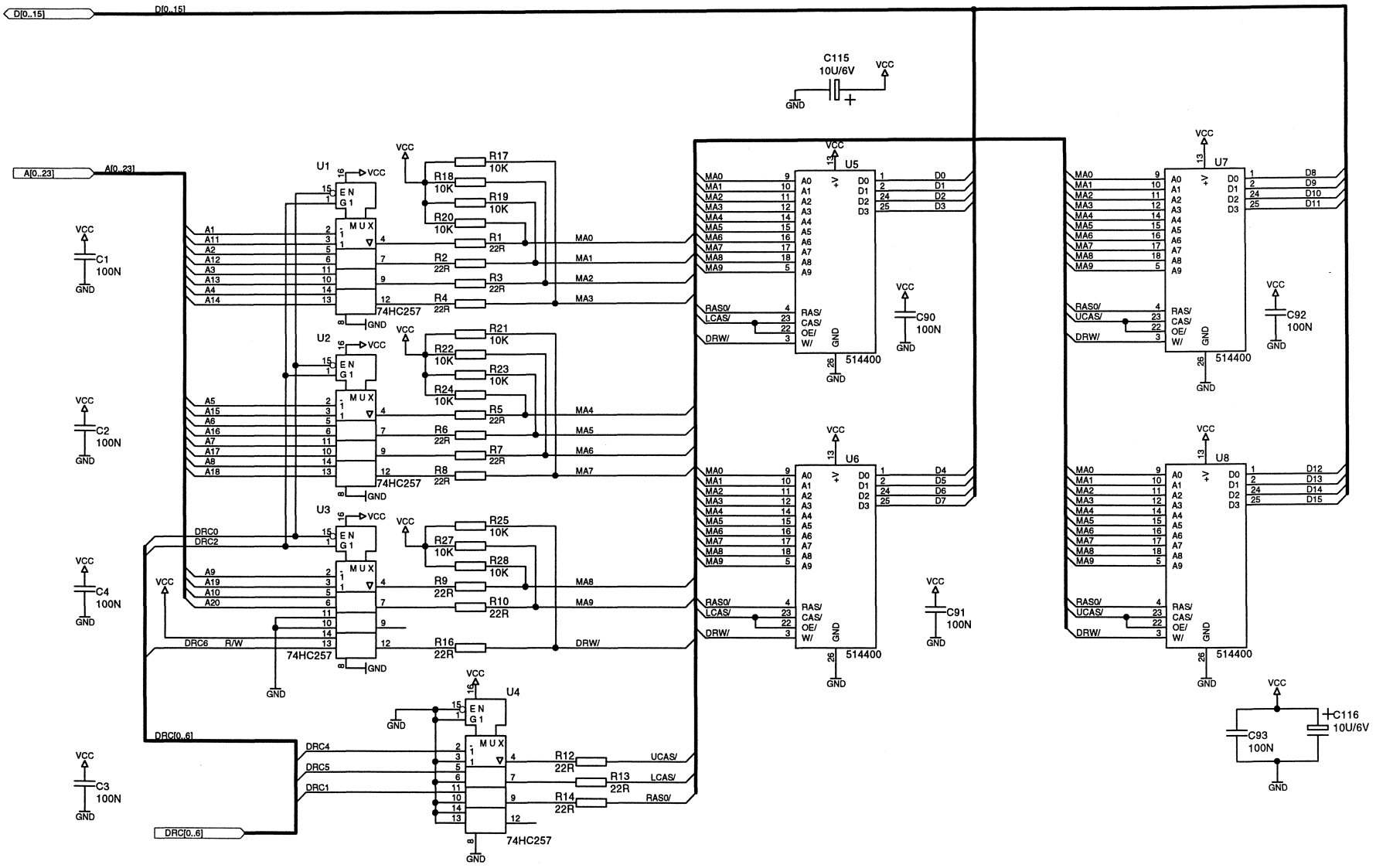
Microprocessor Board MK 8-1



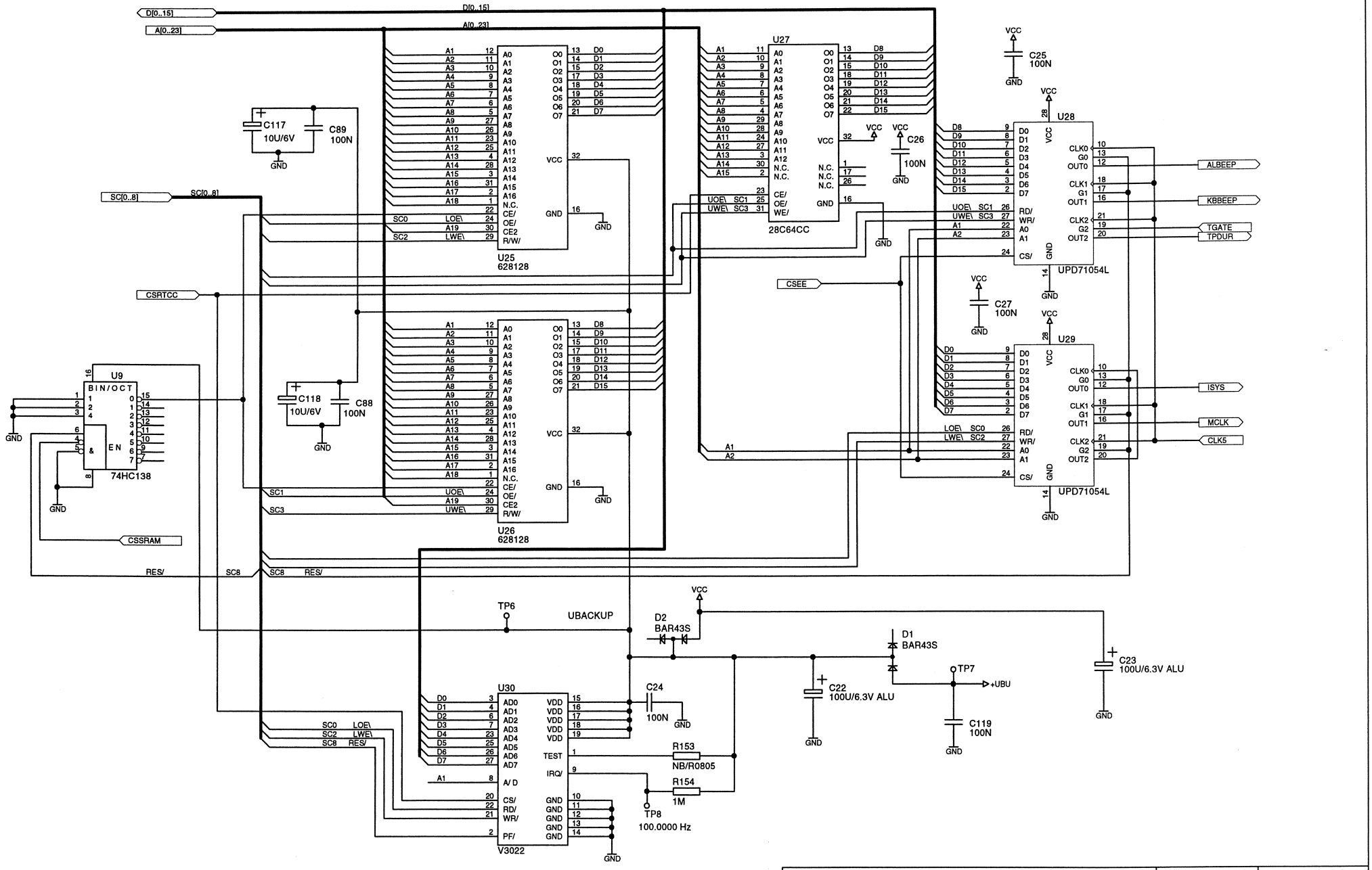
PROCESSOR BOARD AT-10 (MK8-1)				S.2230 FA	Schiller AG Altgasse 68 CH-6340 Baar
Size: A3	Drawn by: STR	Entw: AD	Date: 28.10.98	Sheet 1 of 15	



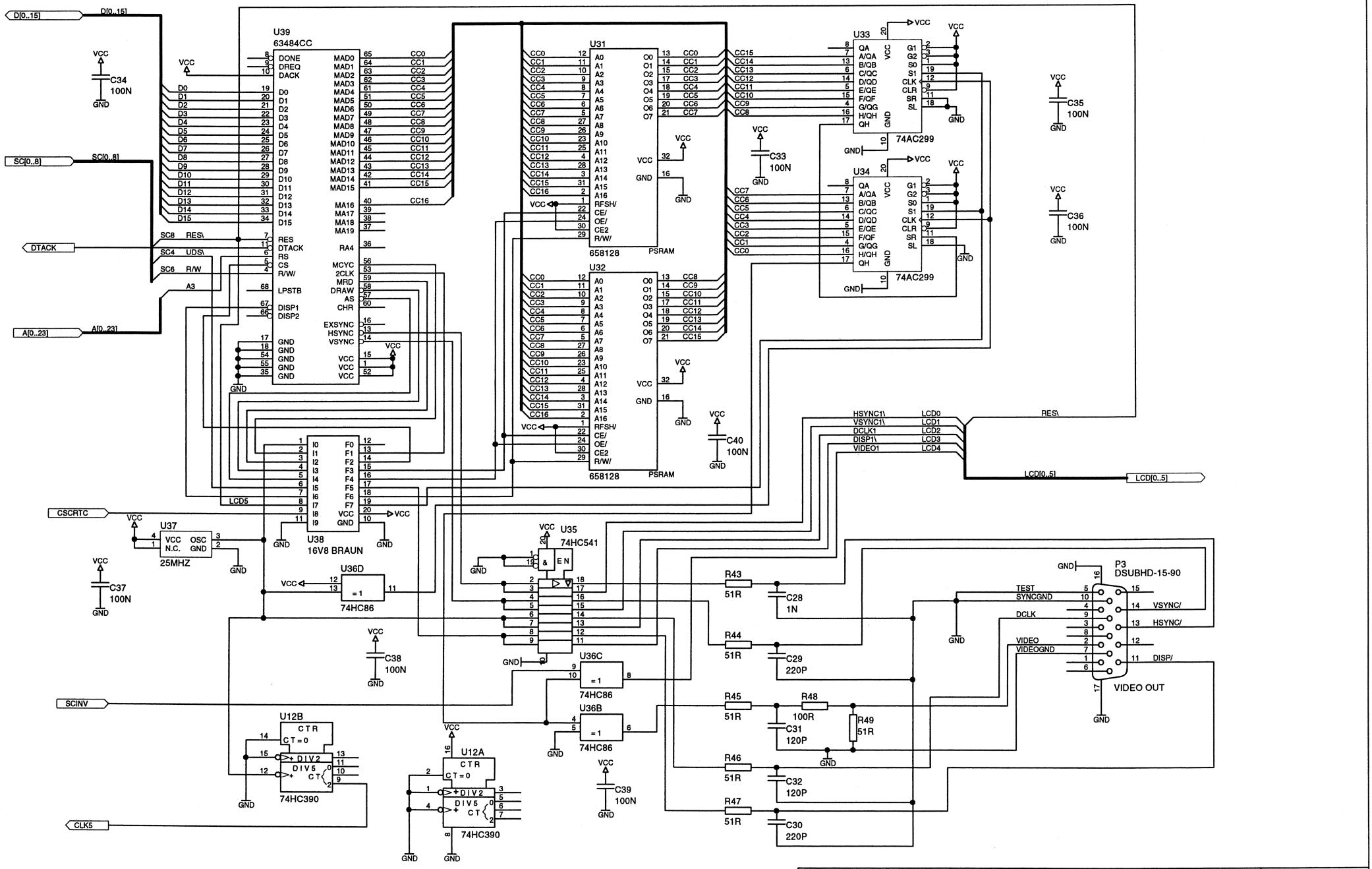
MASTER CPU MK8-1 (AT-10)				S.2230FA	Schiller AG Altgasse 68 CH-6340 Baar
Size: A3	Drawn by: STR	Entw: AD	Date: 28.10.98	Sheet 2 of 15	



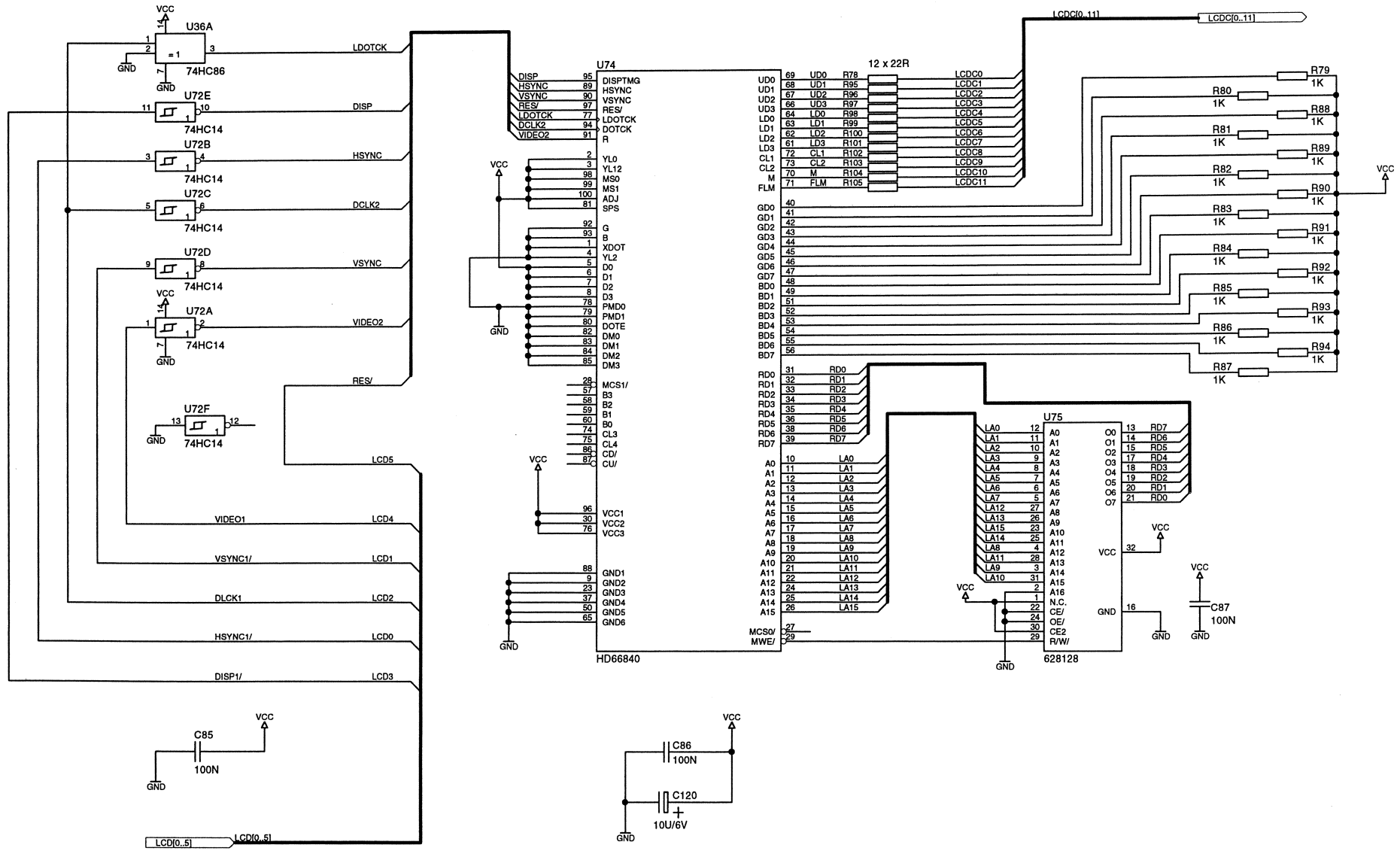
DYNAMIC RAM MK8-1 (AT-10)				S.2230FA	Schiller AG Altgasse 68 CH-6340 Baar
Size: A3	Drawn by: STR	Entw: AD	Date: 28.10-98	Sheet 3 of 15	



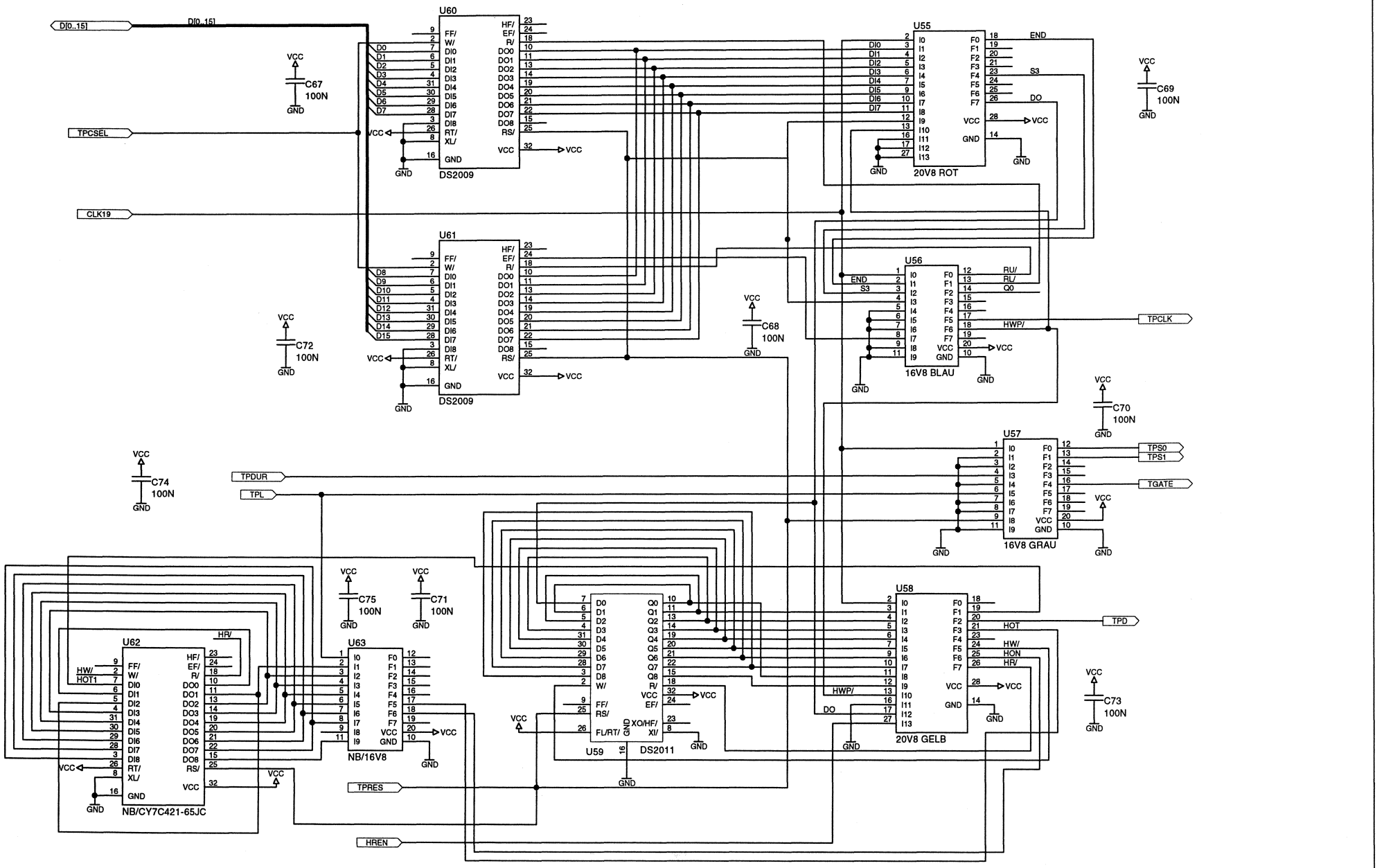
STATIC RAM/EEPROM/TIMER MK8-1 (AT-10)				S.2230FA	Schiller AG Altgasse 68 CH-6340 Baar
Size: A3	Drawn by: STR	Entw: AD	Date: 28.10.98	Sheet 4 of 15	



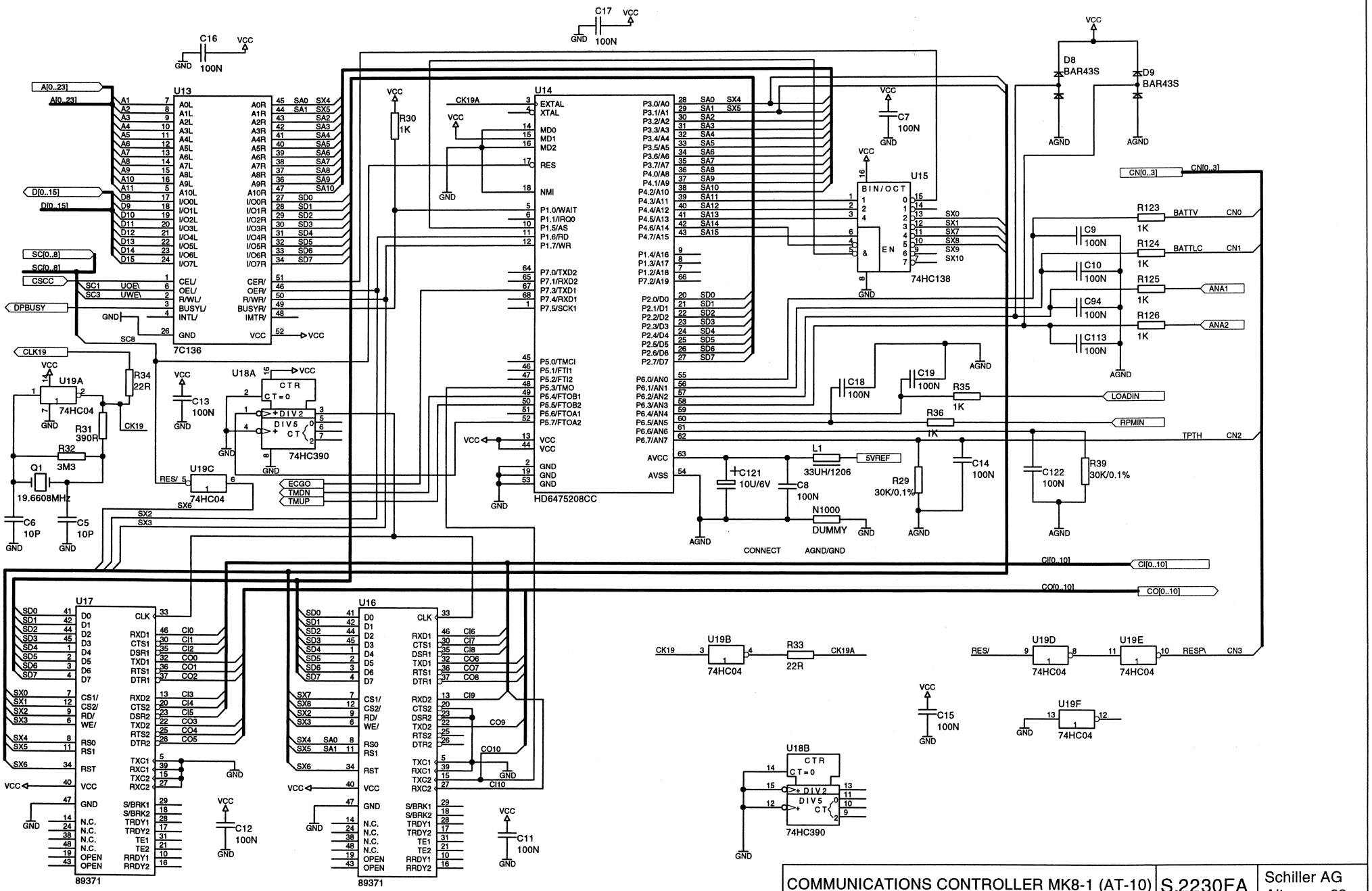
VIDEO CONTROLLER MK8-1 (AT-10)				S.2230FA	Schiller AG Altgasse 68 CH-6340 Baar
Size: A3	Drawn by: STR	Entw: AD	Date: 28.10.98	Sheet 5 of 15	



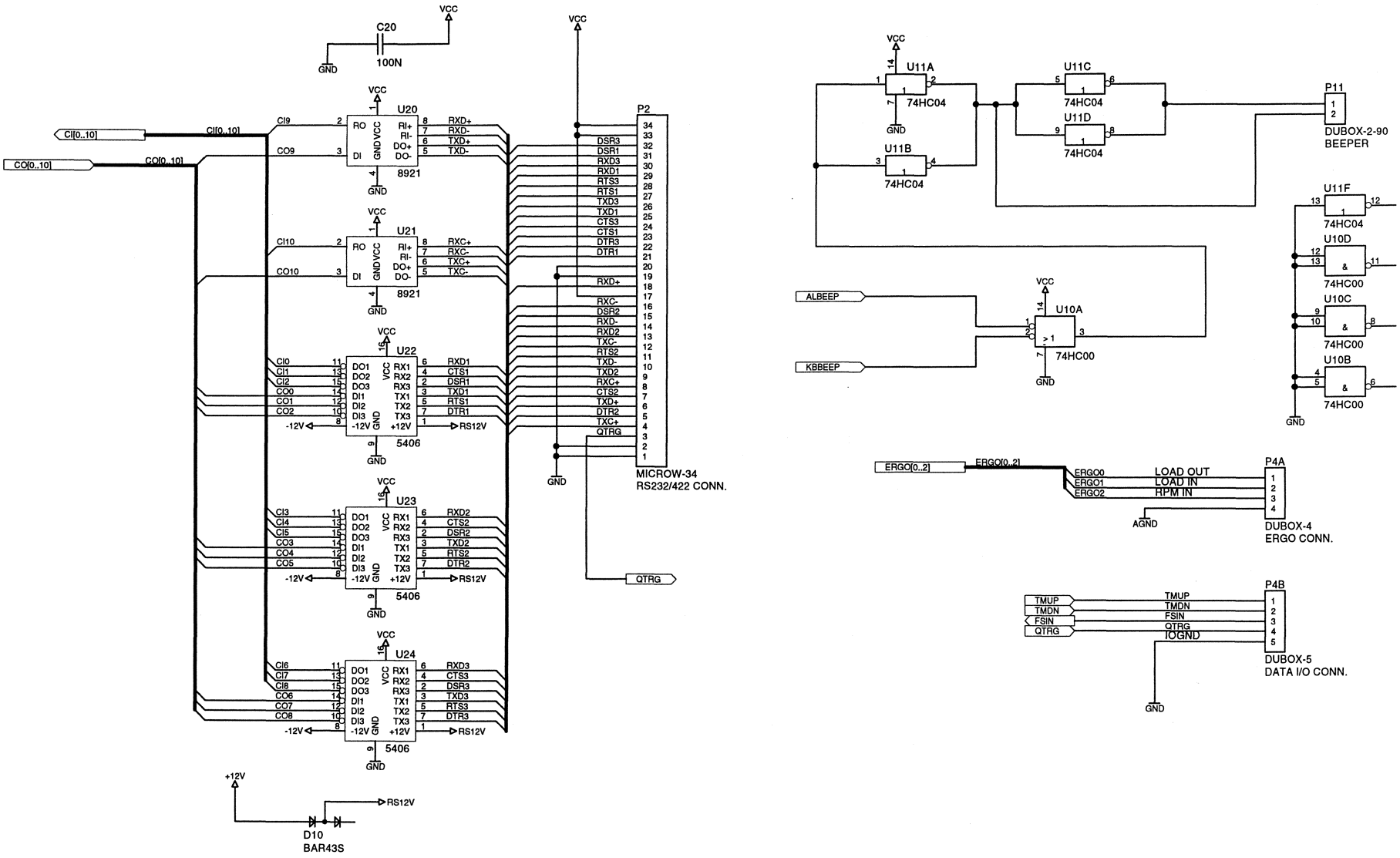
VIDEO-LCD CONVERTER MK8-1 (AT10)				S.2230FA	Schiller AG Altgasse 68 CH-6340 Baar
Size: A3	Drawn by: STR	Entw: AD	Date: 28.10.98	Sheet 6 of 15	



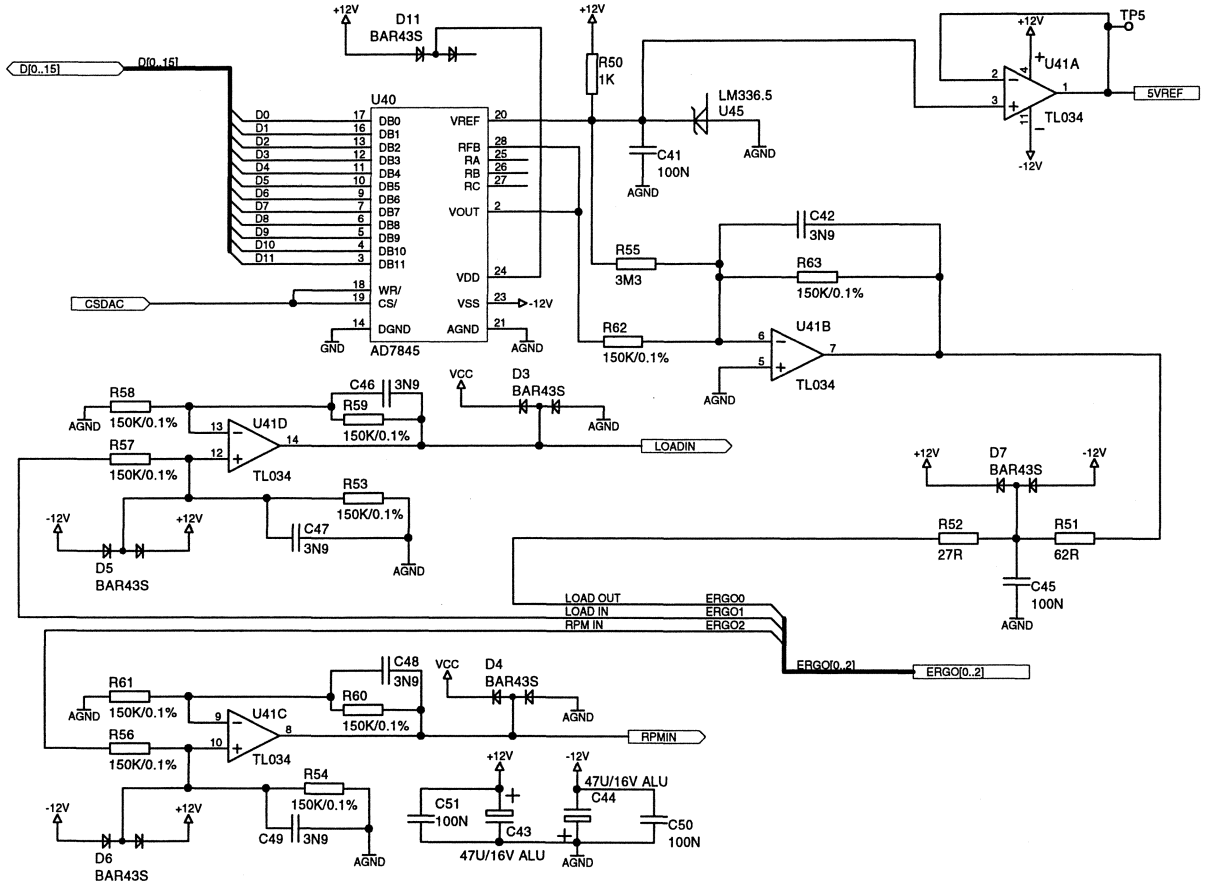
THERMAL PRINT HEAD CONTR. MK8-1 (AT-10)				S.2230FA	Schiller AG Altgasse 68 CH-6340 Baar
Size: A3	Drawn by: STR	Entw: AD	Date: 28.10.98	Sheet 7 of 15	



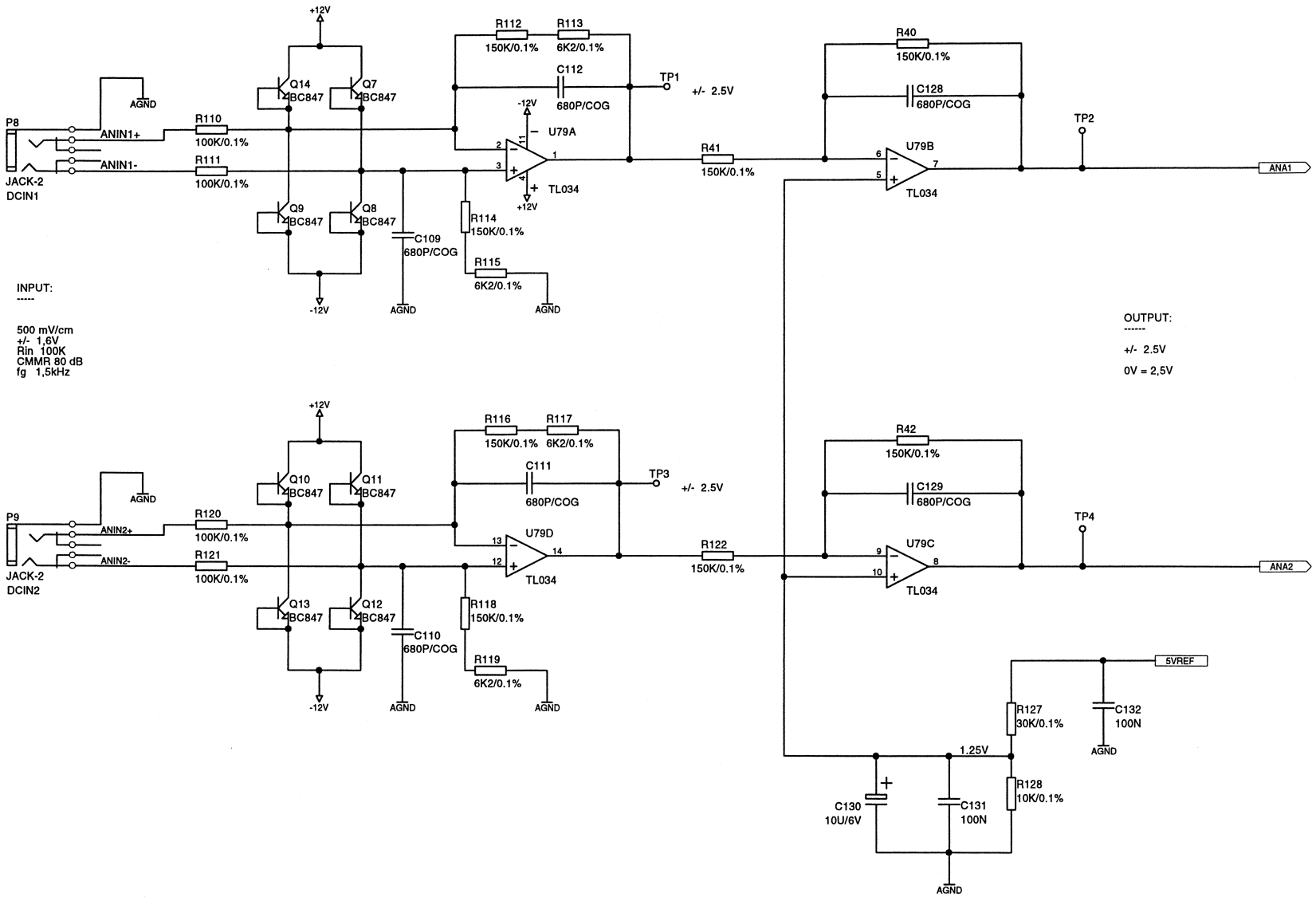
COMMUNICATIONS CONTROLLER MK8-1 (AT-10)				S.2230FA	Schiller AG Altgasse 68 CH-6340 Baar
Size: A3	Drawn by: STR	Entw: AD	Date: 28.10.98	Sheet 8 of 15	



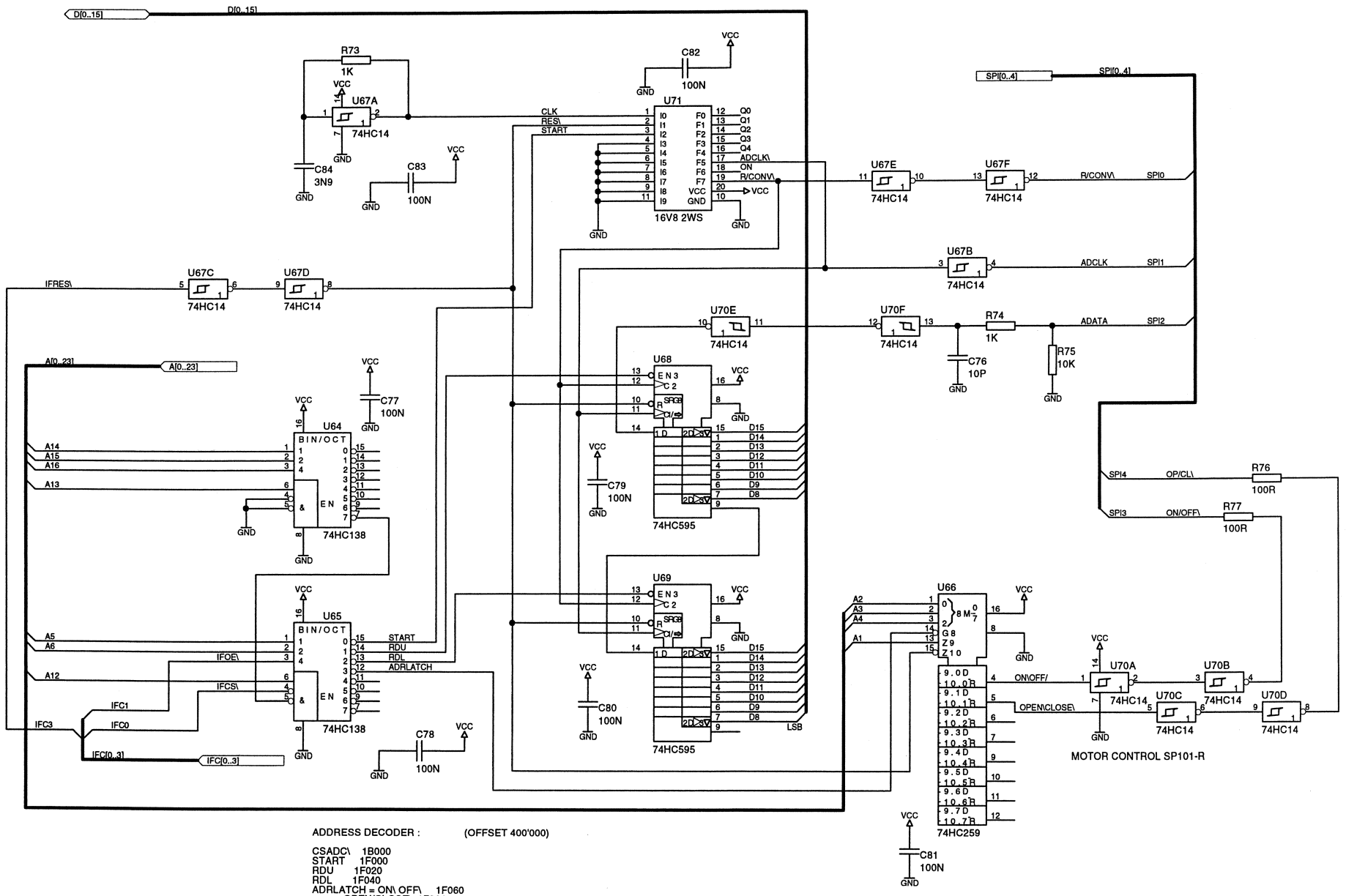
COMMUNICATIONS DRIVER MK8-1 (AT-10)				S.2230FA	Schiller AG Altgasse 68 CH-6340 Baar
Size: A3	Drawn by: STR	Entw: AD	Date: 28.10.98	Sheet 9 of 15	



ANALOG STRESS TEST I/O AT-10 (MK8-1)				S.2230FA	Schiller AG Altgasse 68 CH-6340 Baar
Size: A3	Drawn by: STR	Entw: AD	Date: 28.10.98	Sheet 10 of 15	



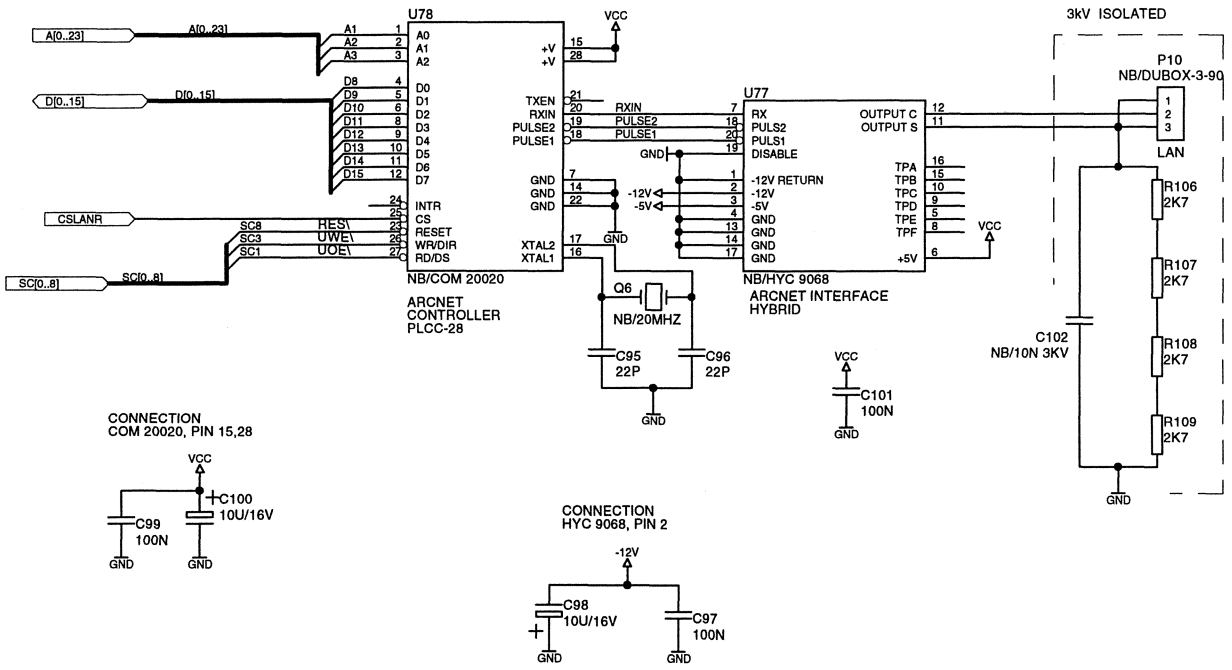
DC - INPUT MK8-1 (AT-10)				S.2230FA	Schiller AG Altgasse 68 CH-6340 Baar
Size: A3	Drawn by: STR	Entw: AD	Date: 28.10.98	Sheet 11 of 15	



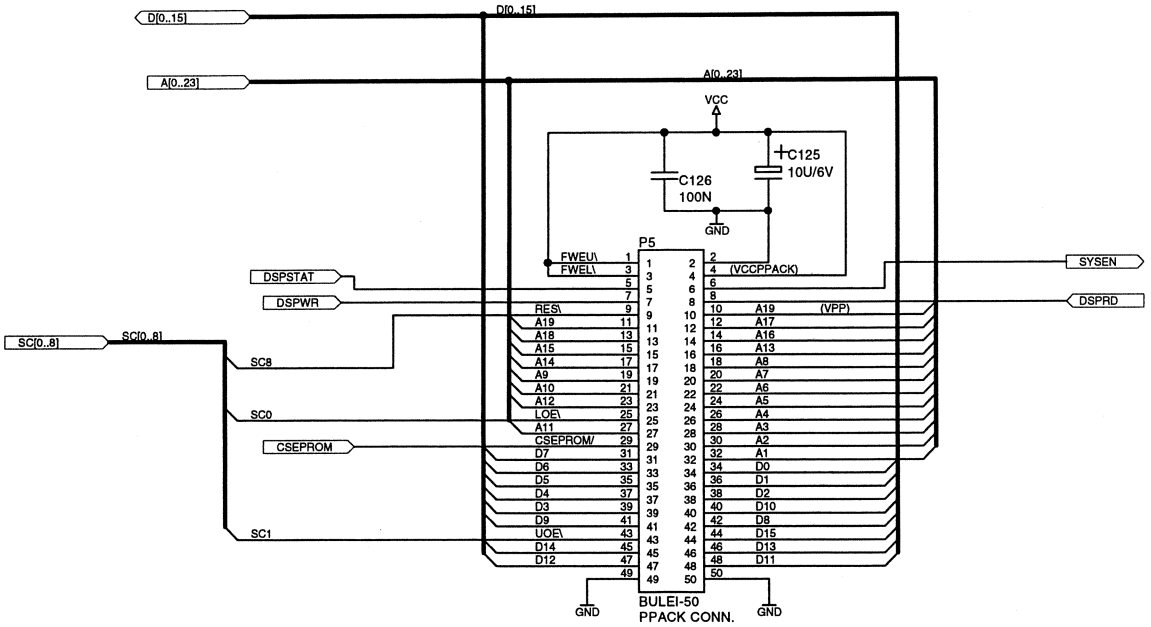
ADDRESS DECODER : (OFFSET 400'000)

CSADC\ 1B000
START 1F000
RDU 1F020
RDL 1F040
ADRLATCH = ON\ OFF\ 1F060
OPEN/CLOSE\ 1F064

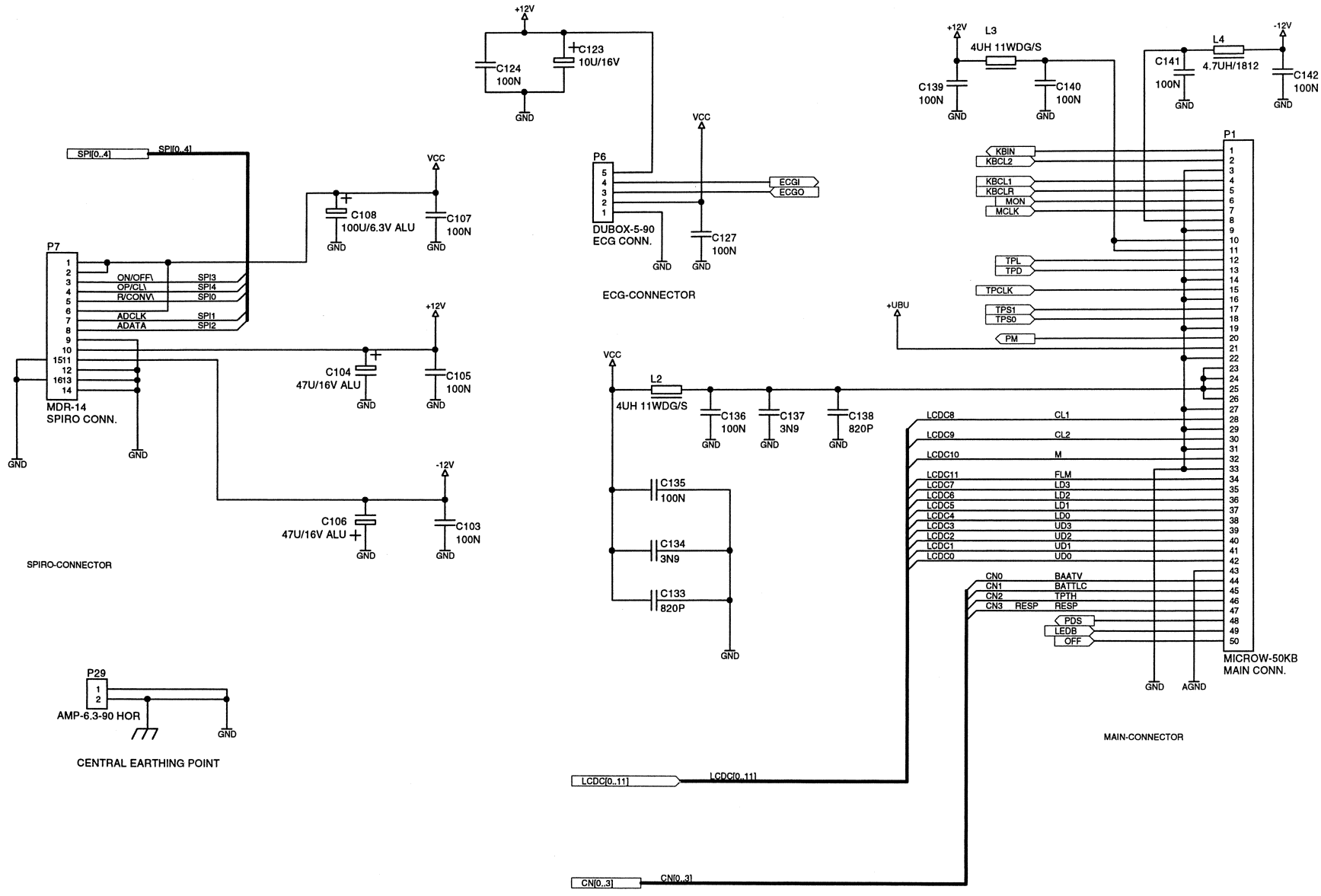
INTERFACE SP101 / R MK8-1 (AT-10)				S.2230FA	Schiller AG Altgasse 68 CH-6340 Baar
Size: A3	Drawn by: STR	Entw: AD	Date: 28.10.98	Sheet 12 of 15	



LAN CONTROLLER MK8-1 (AT-10)				S.2230FA	Schiller AG Altgasse 68 CH-6340 Baar
Size: A3	Drawn by: STR	Entw: AD	Date: 28.10.98	Sheet 13 of 15	



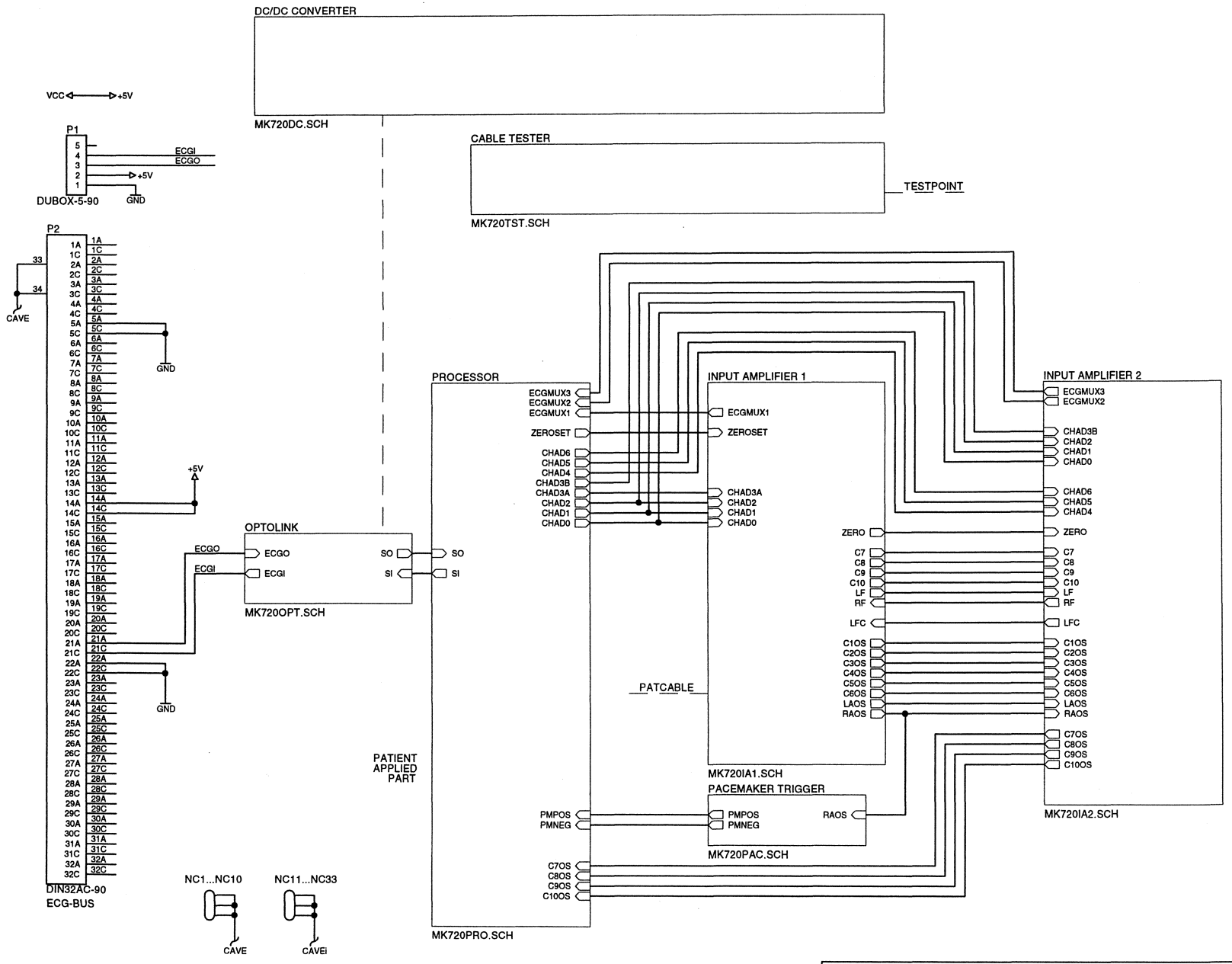
PROGRAM PACK CONNECTOR MK8-1 (AT-10)				S.2230FA	Schiller AG Altgasse 68 CH-6340 Baar
Size: A3	Drawn by: STR	Entw: AD	Date: 28.10.98	Sheet 14 of 15	



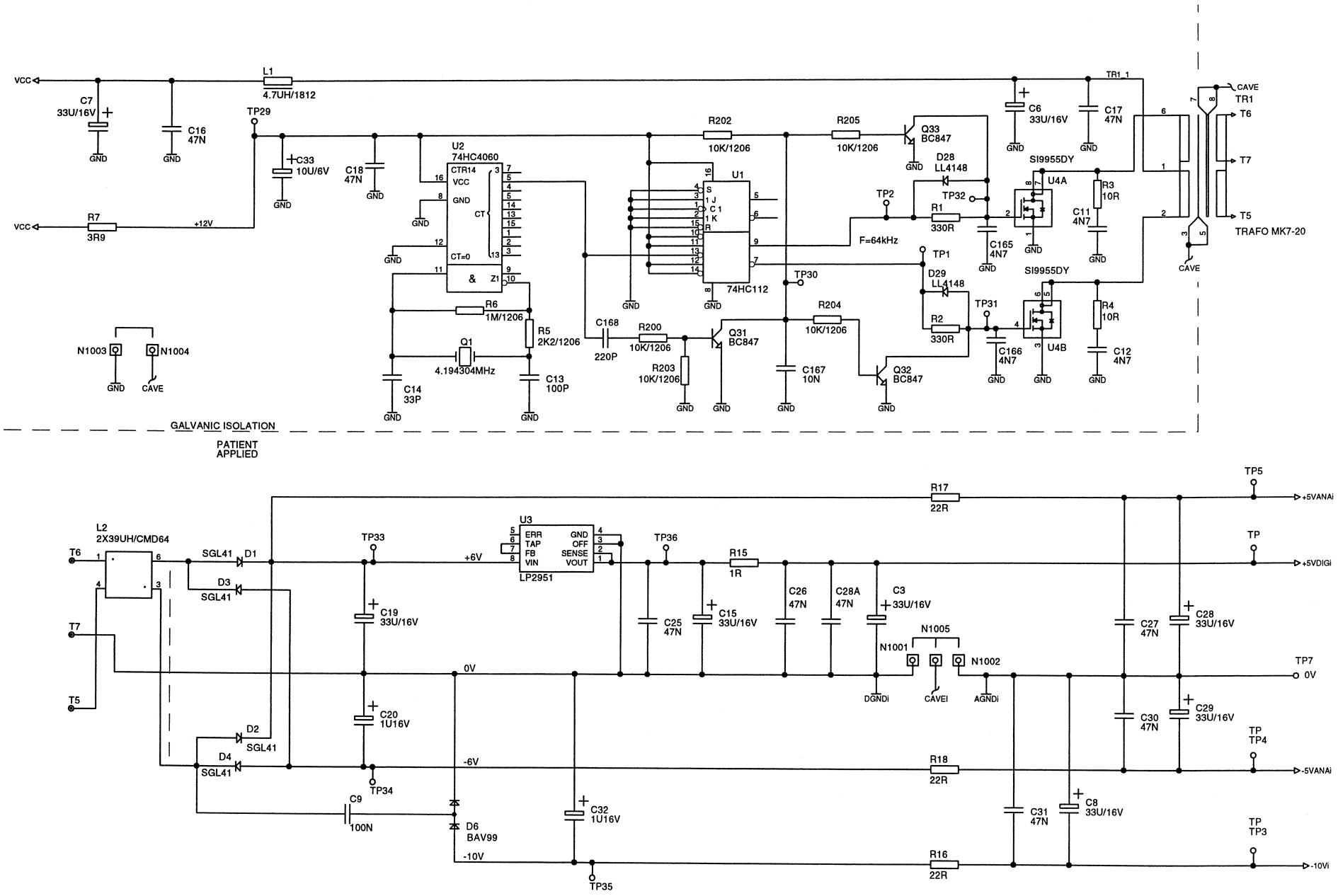
SPIRO / MAIN-CONNECTOR MK8-1 (AT-10)				S.2230FA	Schiller AG Altgasse 68 CH-6340 Baar
Size: A3	Drawn by: STR	Entw: AD	Date: 28.10.98	Sheet 15 of 15	

AT-10 Circuit Diagrams and Board Layouts

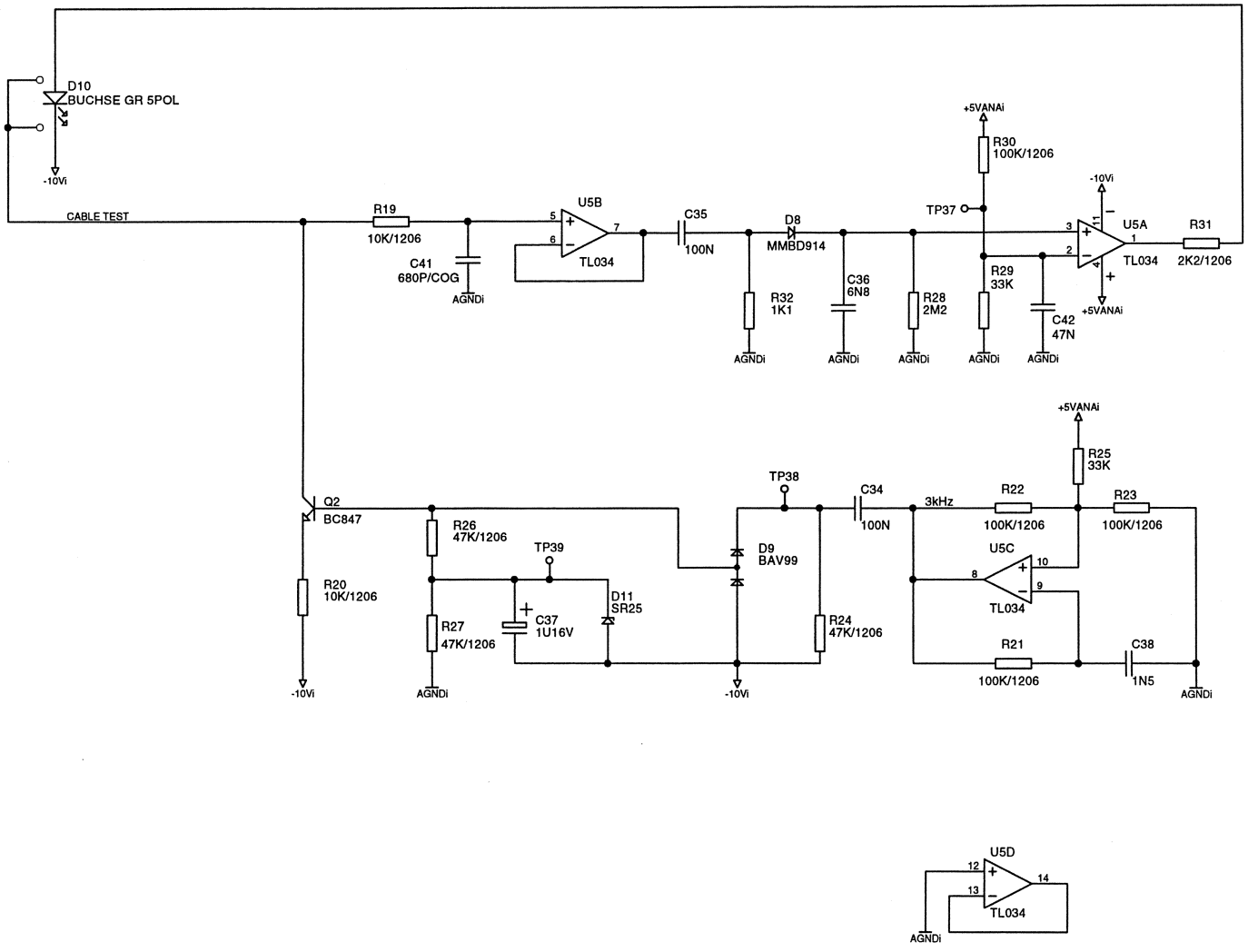
ECG Amplifier MK 7-20



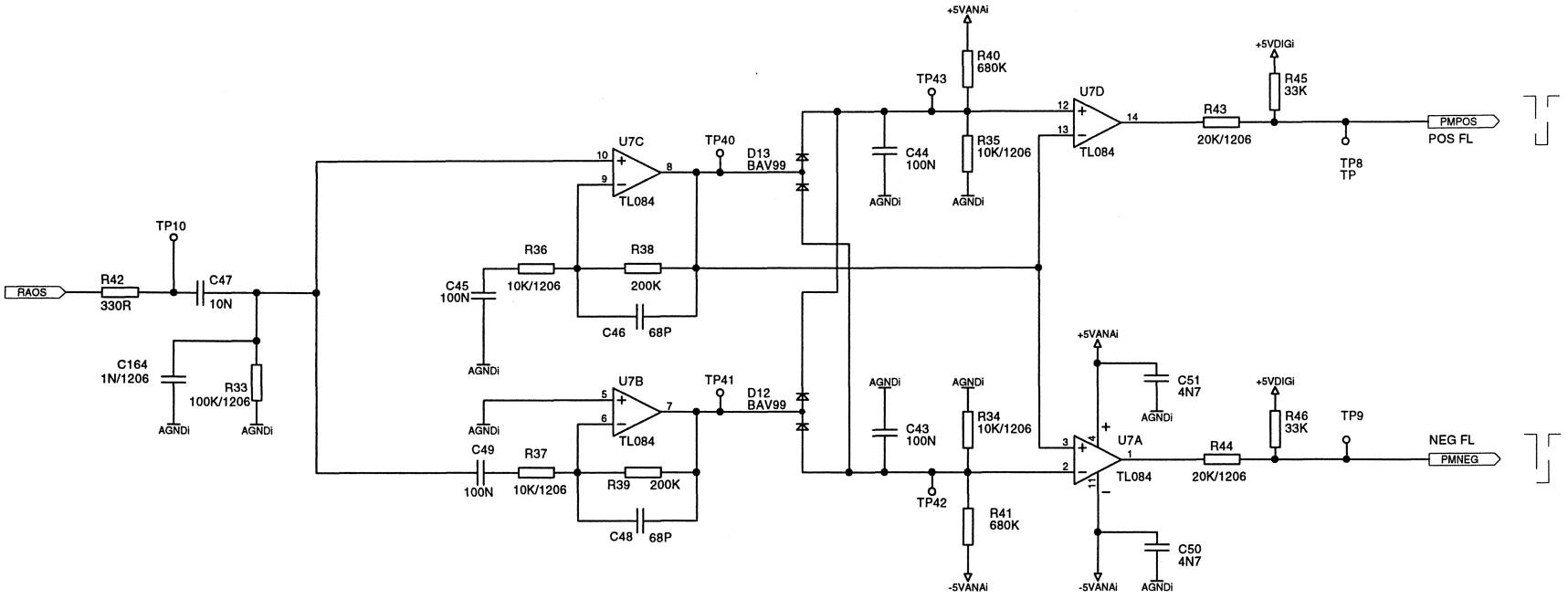
ECG INTERFACE MK7-20 (CS-100)				S.2223DB	Schiller AG Altgasse 68 CH-6340 Baar
Size: A3	Drawn by: STR	Entw: HSCH	Date: 09.02.99	Sheet 1 of 8	



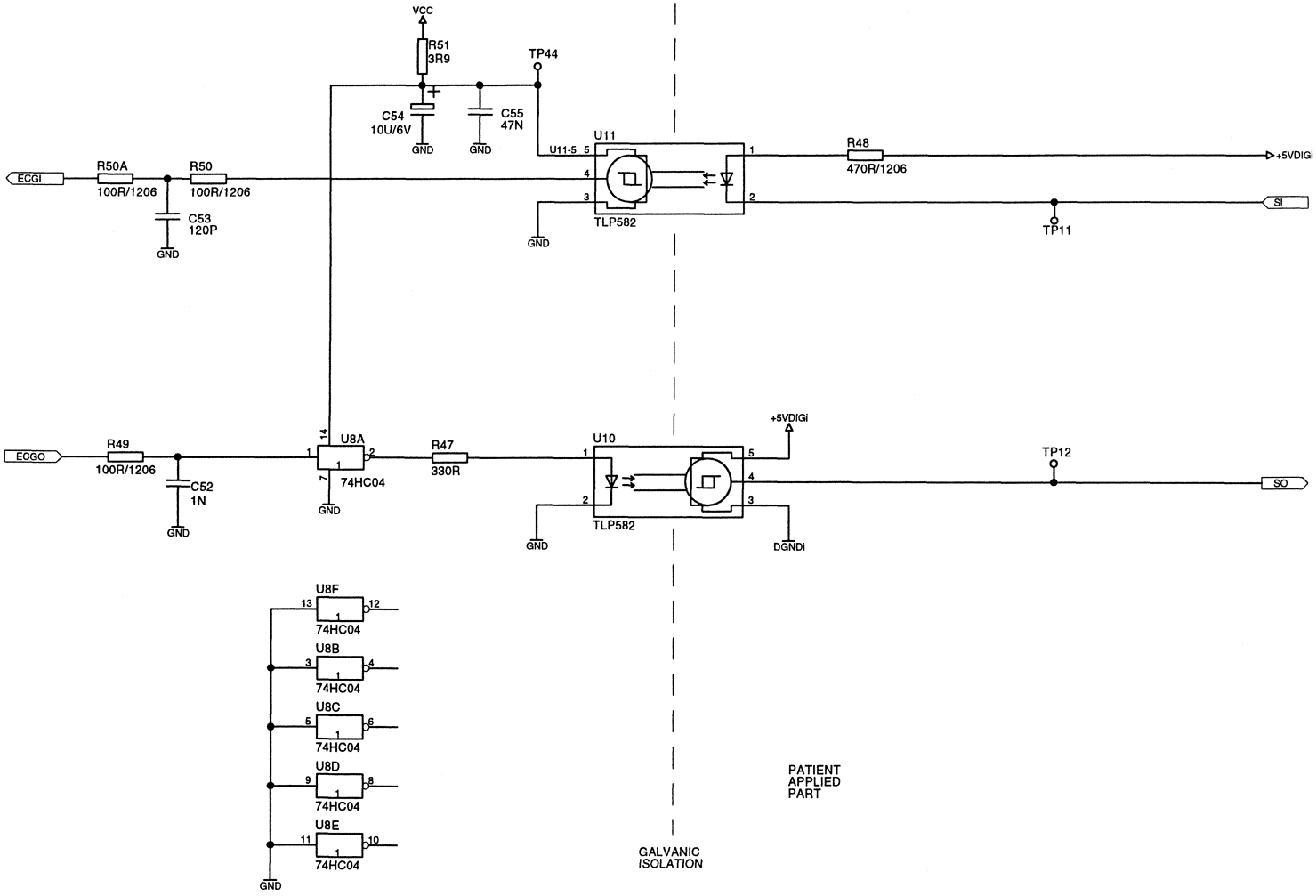
DC/DC CONVERTER MK7-20 (CS-100)				S.2223DB	Schiller AG Altgasse 68 CH-6340 Baar
Size: A3	Drawn by: STR	Entw: HSCH	Date: 09.02.99	Sheet 2 of 8	



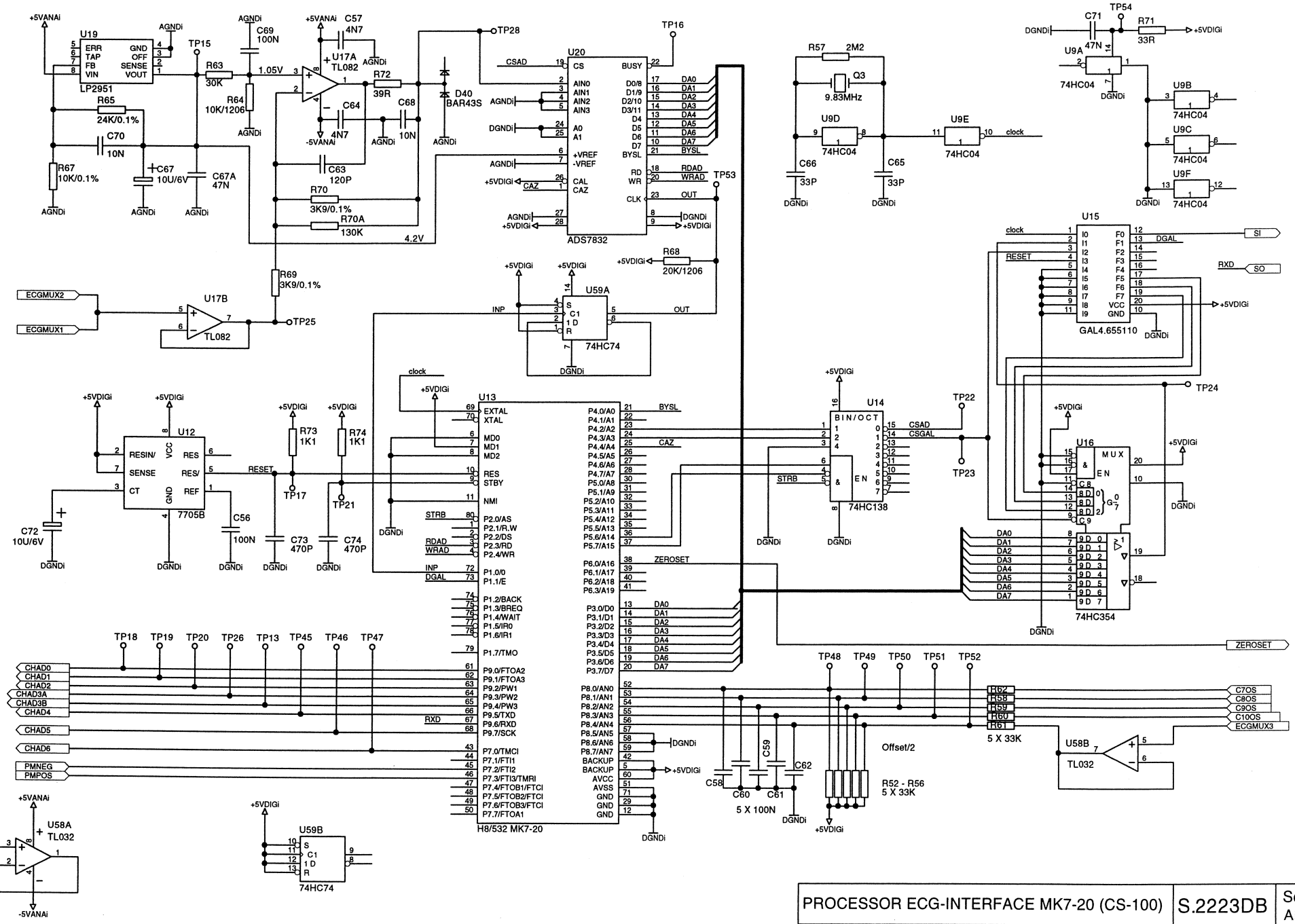
CABLE TESTER MK7-20 (CS-100)				S.2223DB	Schiller AG Altgasse 68 CH-6340 Baar
Size: A3	Drawn by: STR	Entw: HSCH	Date: 09.02.99	Sheet 3 of 8	



ECG PACEMAKER TRIGGER MK7-20 (CS-100)				S.2223DB	Schiller AG Altgasse 68 CH-6340 Baar
Size: A3	Drawn by: STR	Entw: HSCH	Date: 09.02.99	Sheet 4 of 8	



OPTOLINK MK7-20 (CS-100)				S.2223DB	Schiller AG Altgasse 68 CH-6340 Baar
Size: A3	Drawn by: STR	Entw: HSCH	Date: 09.02.99	Sheet 5 of 8	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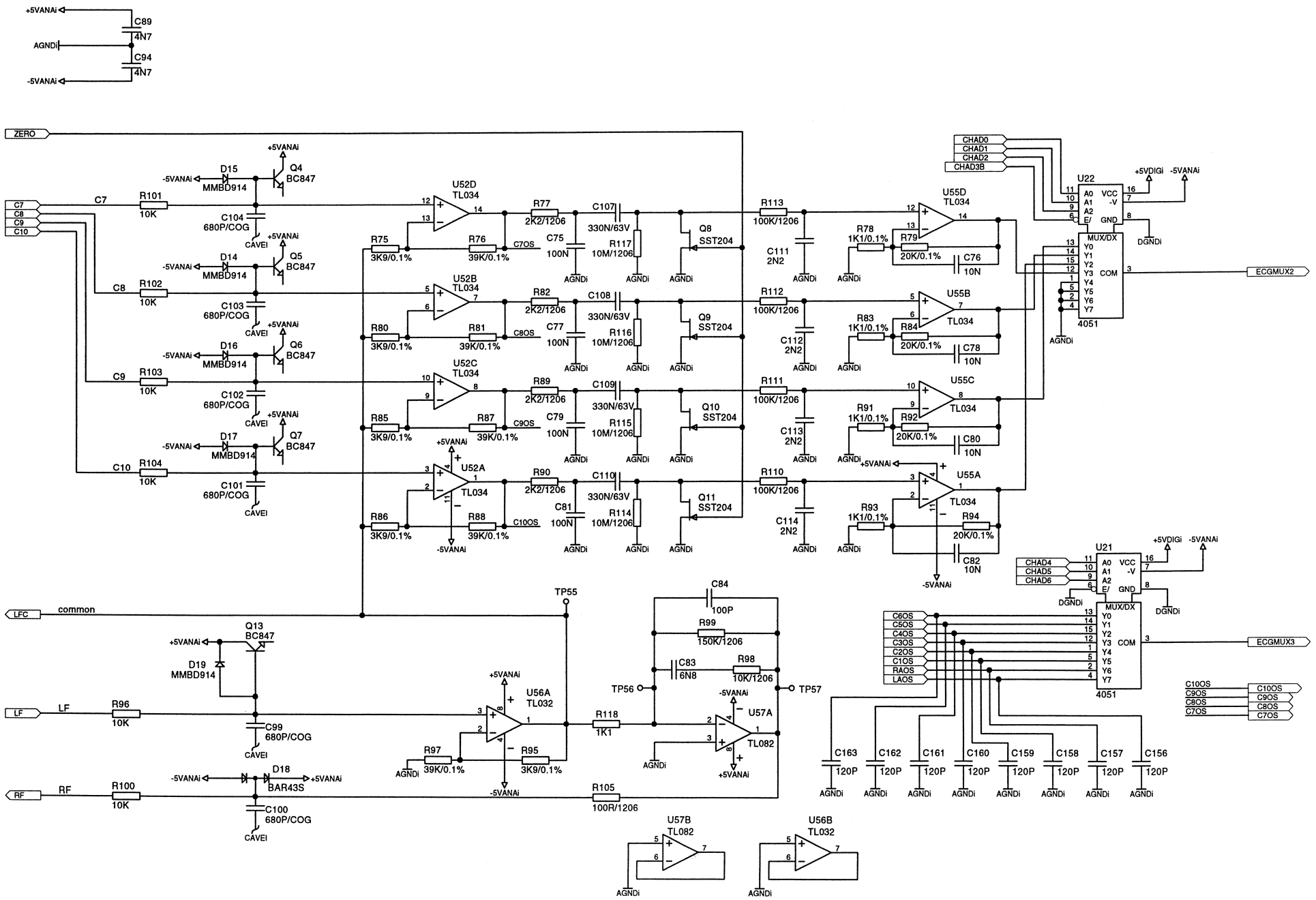
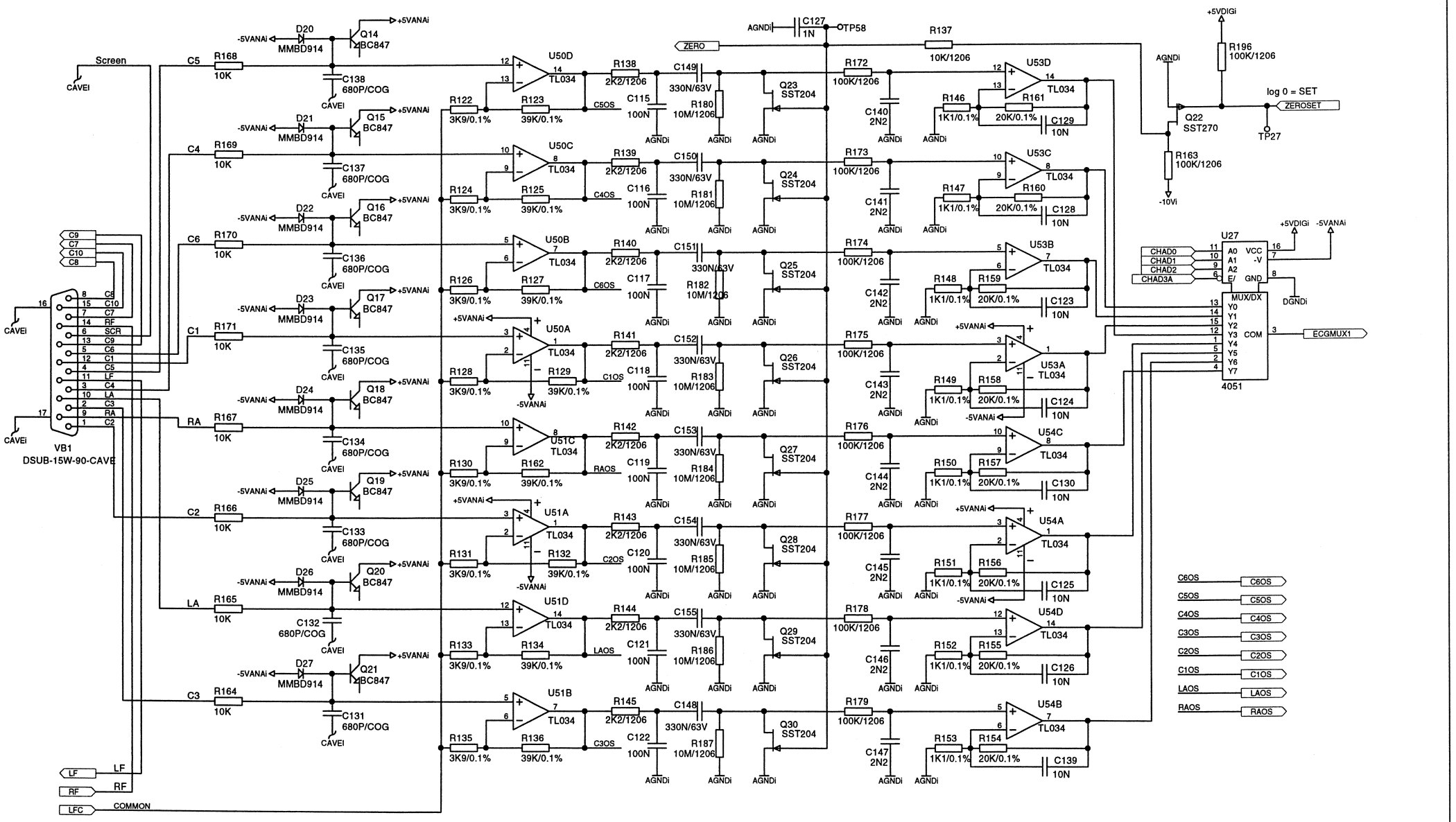
LP2951
ERR
TAP
FB
VIN
GND
OFF
SENSE
VOUT
+5VANAi
AGNDi
TP15
R63
30K
1.05V
C69
100N
R64
10K/1206
R65
24K/0.1%
C70
10N
R67
10K/0.1%
C67
10U/6V
C67A
47N
U17A
TL082
C57
4N7
C64
4N7
-5VANAi
R72
39R
C68
10N
C63
120P
R70
3K9/0.1%
R70A
130K
4.2V
D40
BAR43S
TP28
TP16
U20
ADS7832
CSAD
CS
BUSY
AIN0
AIN1
AIN2
AIN3
A0
A1
+VREF
-VREF
CAL
CAZ
RD
WR
CLK
RDAD
WRAD
OUT
BYSL
DA0
DA1
DA2
DA3
DA4
DA5
DA6
DA7
DGNDi
+5VDIGi
TP53
R68
20K/1206
R69
3K9/0.1%
ECGMUX2
ECGMUX1
U17B
TL082
TP25
U59A
74HC74
INP
S
C1
1 D
R
OUT
clock
U13
H8/532 MK7-20
EXTAL
XTAL
MD0
MD1
MD2
RES
STBY
NMI
P2.0/AS
P2.1/R.W
P2.2/DS
P2.3/RD
P2.4/WR
P1.0/0
P1.1/E
P1.2/BACK
P1.3/BREQ
P1.4/WAIT
P1.5/IR0
P1.6/IR1
P1.7/TMO
P9.0/FTOA2
P9.1/FTOA3
P9.2/PW1
P9.3/PW2
P9.4/PW3
P9.5/TXD
P9.6/RXD
P9.7/SCK
P7.0/TMCI
P7.1/FTI1
P7.2/FTI2
P7.3/FTI3/TMRI
P7.4/FTOB1/FTCI
P7.5/FTOB2/FTCI
P7.6/FTOB3/FTCI
P7.7/FTOA1
P4.0/A0
P4.1/A1
P4.2/A2
P4.3/A3
P4.4/A4
P4.5/A5
P4.6/A6
P4.7/A7
P5.0/A8
P5.1/A9
P5.2/A10
P5.3/A11
P5.4/A12
P5.5/A13
P5.6/A14
P5.7/A15
P6.0/A16
P6.1/A17
P6.2/A18
P6.3/A19
P3.0/D0
P3.1/D1
P3.2/D2
P3.3/D3
P3.4/D4
P3.5/D5
P3.6/D6
P3.7/D7
P8.0/AN0
P8.1/AN1
P8.2/AN2
P8.3/AN3
P8.4/AN4
P8.5/AN5
P8.6/AN6
P8.7/AN7
BACKUP
AVCC
AVSS
GND
STRB
RDAD
WRAD
INP
DGAL
RXD
CAZ
ZEROSET
U12
7705B
RESIN/
SENSE
CT
VCC
RES
RES/
REF
GND
RESET
C72
10U/6V
C56
100N
R73
1K1
R74
1K1
C73
470P
C74
470P
TP17
TP21
U14
74HC138
BIN/OCT
EN
CSAD
CSGAL
TP22
TP23
R57
2M2
Q3
9.83MHz
C66
33P
C65
33P
U9D
U9E
74HC04
clock
TP54
C71
47N
R71
33R
U9A
U9B
U9C
U9F
U15
GAL4.655110
RESET
SI
SO
DGAL
RXD
VCC
GND
U16
74HC354
MUX
EN
TP24
TP18
TP19
TP20
TP26
TP13
TP45
TP46
TP47
CHAD0
CHAD1
CHAD2
CHAD3A
CHAD3B
CHAD4
CHAD5
CHAD6
PMNEG
PMPOS
TP48
TP49
TP50
TP51
TP52
R62
R55
R59
R60
R61
5 X 33K
C7OS
C8OS
C9OS
C10OS
ECGMUX3
U58B
TL032
C58
C60
5 X 100N
C59
C61
C62
Offset/2
R52 - R56
5 X 33K
U58A
TL032
-5VANAi
U59B
74HC74
PROCESSOR ECG-INTERFACE MK7-20 (CS-100)
S.2223DB
Schiller AG
Altgasse 68
CH-6340 Baar
Size: A3
Drawn by: STR
Entw: HSCH
Date: 09.02.99
Sheet 6 of 8

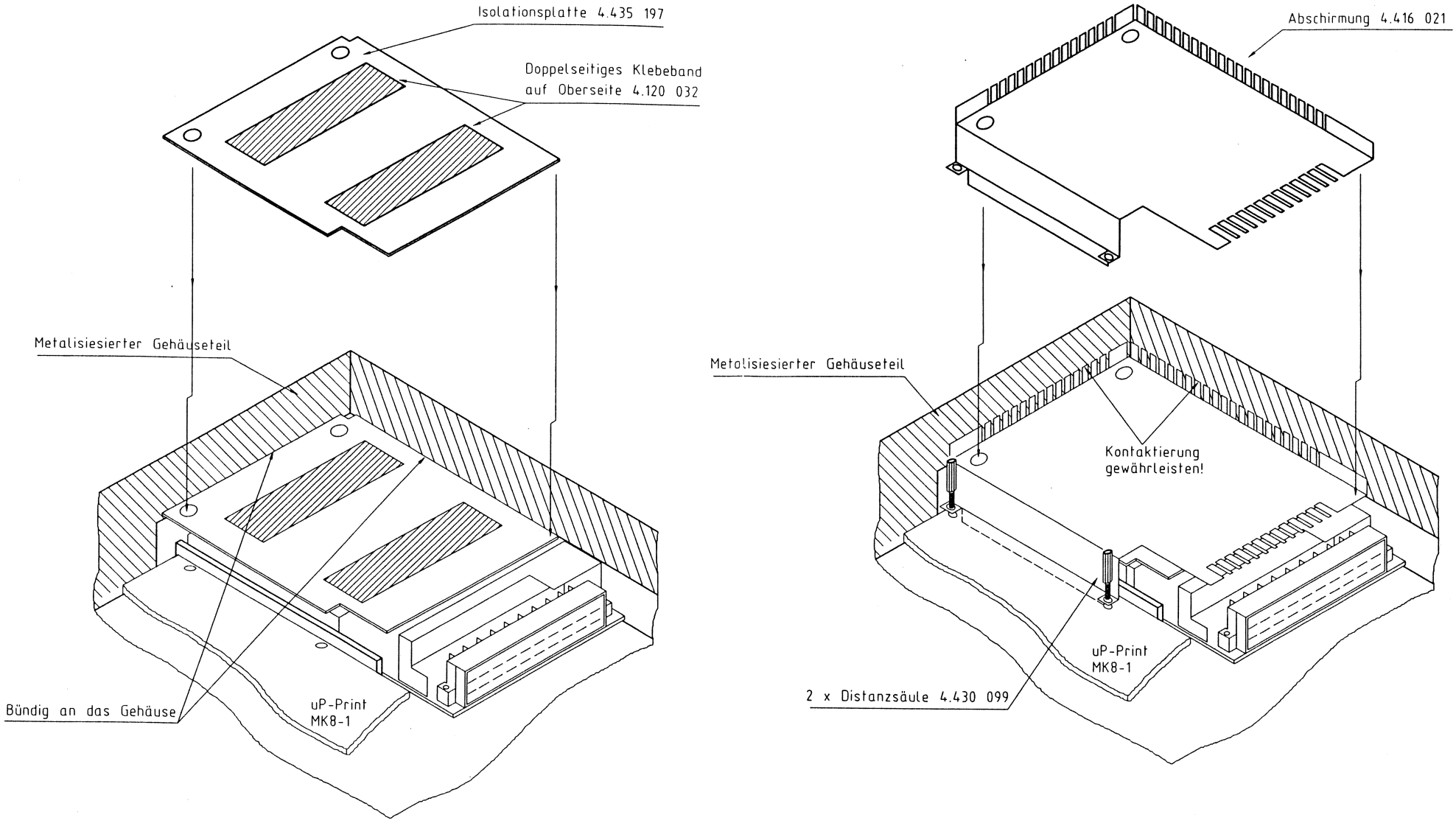


INPUT AMPLIFIER 2 MK7-20 (CS-100)				S.2223DB	Schiller AG Altgasse 68 CH-6340 Baar
Size: A3	Drawn by: STR	Entw: HSCH	Date: 09.02.99	Sheet 7 of 8	



INPUT AMPLIFIER 1 MK7-20 (CS-100)				S.2223DB	Schiller AG Altgasse 68 CH-6340 Baar
Size: A3	Drawn by: STR	Entw: HSCH	Date: 09.02.99	Sheet 8 of 8	

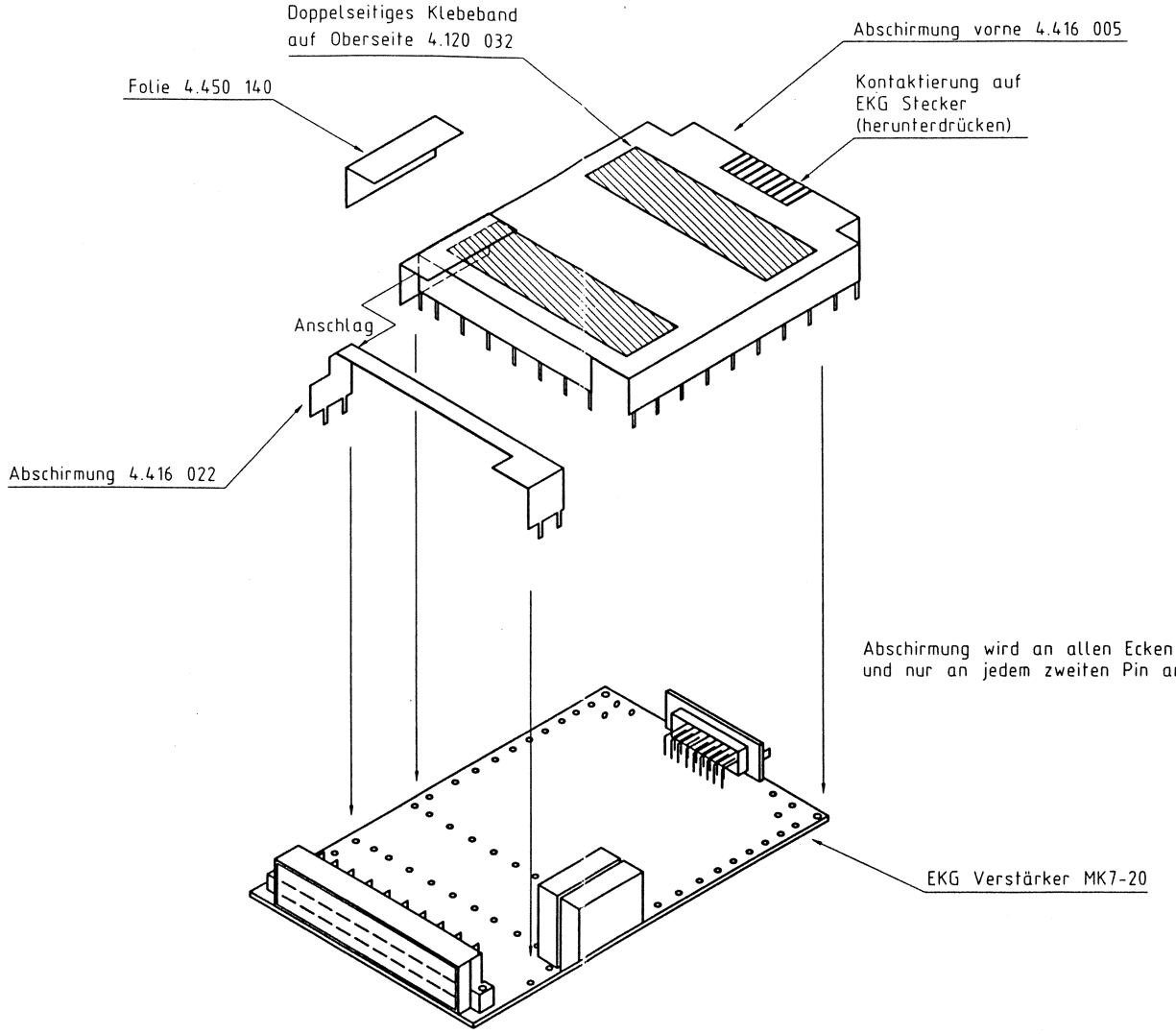
KOPIE



Pos.	Menge	Gegenstand	Werkstoff		Oberfläche		Bemerkung		
					Gezeichnet	2. Dezember 1994	HSCH/rr	Massstab	1:2
					Gepräft	6.12.94			
					BG-Zuordnung	AT-10			
					Ersatz für		Anzahl Blatt	Blatt-Nr.	
Index		Aenderung	Datum	Sig.	File: AT10\FMK7-20	A3			

SCHILLER AG CH-6340 BAAR	Titel: Montage Isol. u. Abschirm.	Zeichnungs-Nr.: F. MK7-20

KOPIE

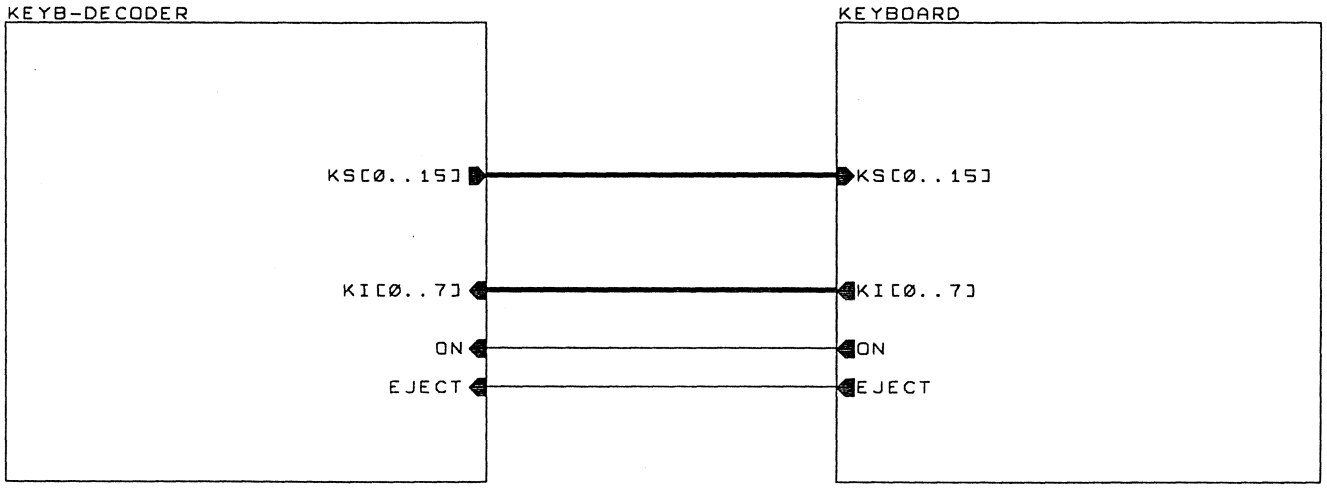


Pos.	Menge	Gegenstand	Werkstoff		Oberfläche		Bemerkung	
					Gezeichnet	5. Dezember 1994	HSCH/rr	Massstab
					Geprüft	16.12.94	Kfil	
					BG-Zuordnung	AT-10		
a	Neu Folie 4.450 140 ankleben		14.12.94	HSch/rr	Ersatz für		Anzahl Blatt	Blatt-Nr.
Index	Aenderung		Datum	Sig.	File: AT10\FMK7-201	A3		

SCHILLER AG CH-6340 BAAR	Titel: Mont. Abschirm. EKG	Zeichnungs-Nr.: F.MK7-20/1a

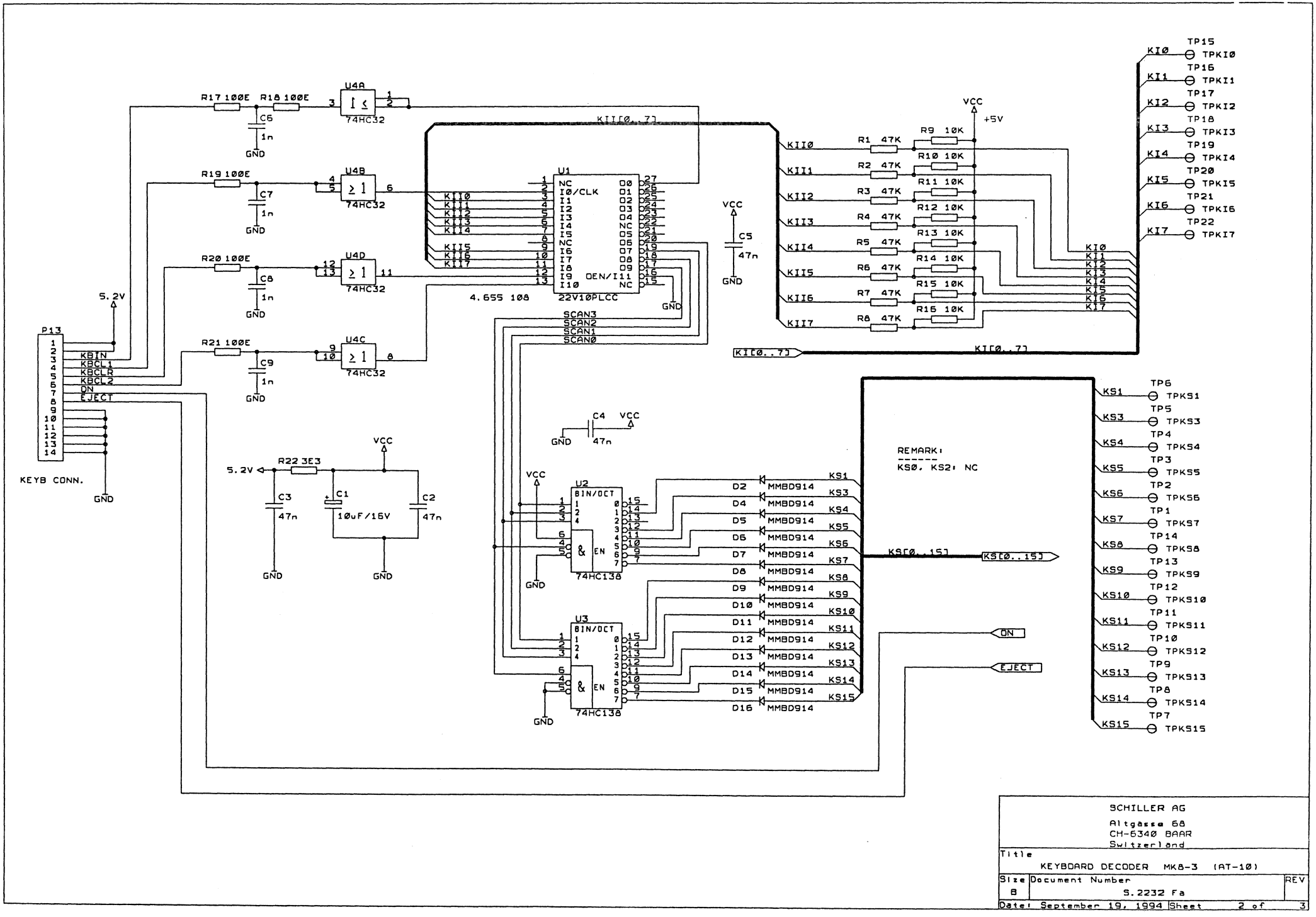
AT-10 Circuit Diagrams and Board Layouts

Keyboard MK 8-3



SCHILLER AG
Altgasse 68
CH-6340 BAAR
Switzerland

Title		
KEYBOARD MK8-3 (AT-10)		
Size: A	Document Number: S.2232 Fa	REV:
Date: September 19, 1994	Sheet 1 of 3	

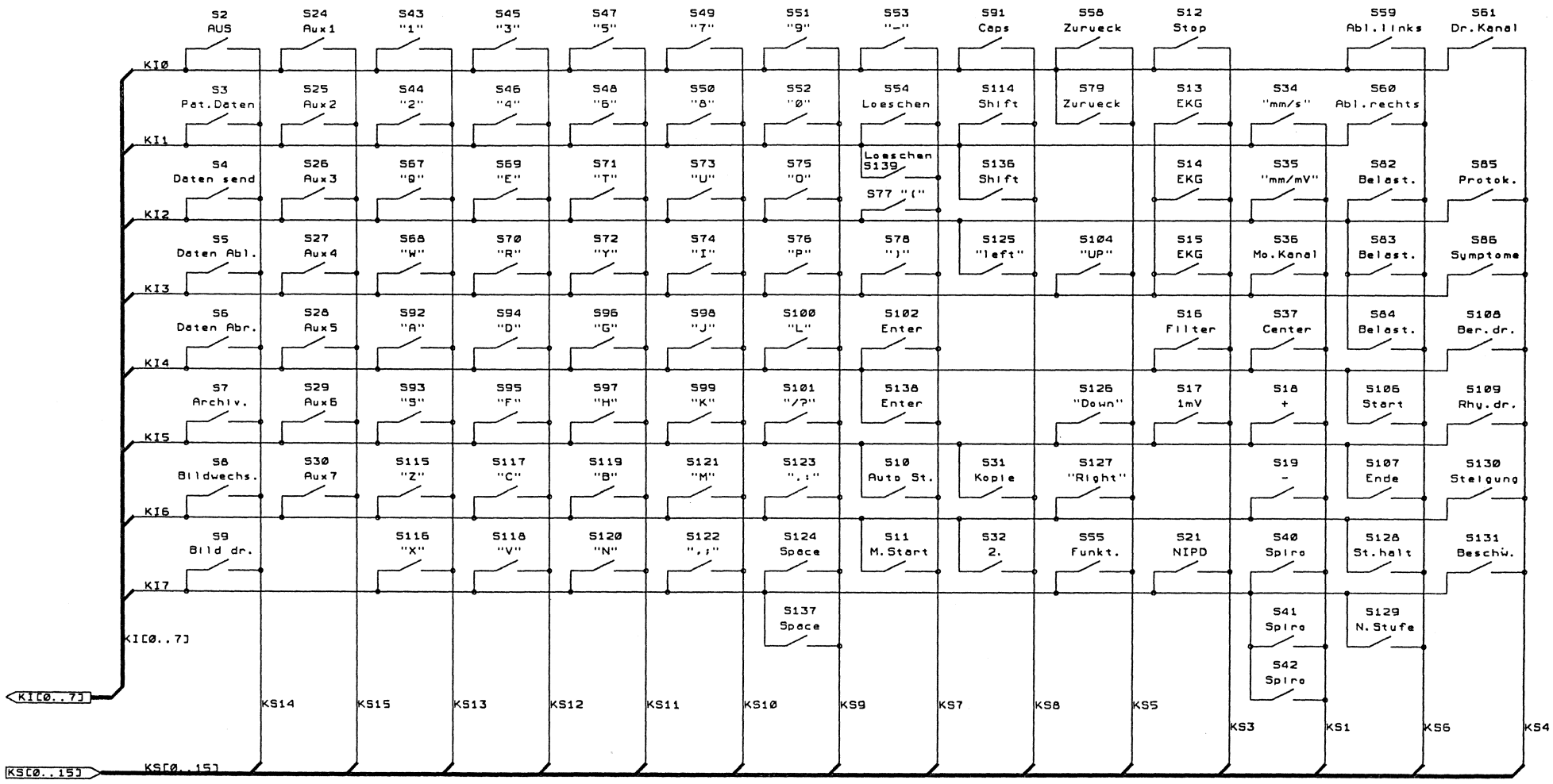


SCHILLER AG
Altgasse 68
CH-6340 BAAR
Switzerland

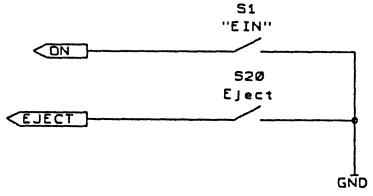
Title: KEYBOARD DECODER MK8-3 (AT-10)

Size: B | Document Number: S.2232 Fa | REV:

Date: September 19, 1994 | Sheet 2 of 3



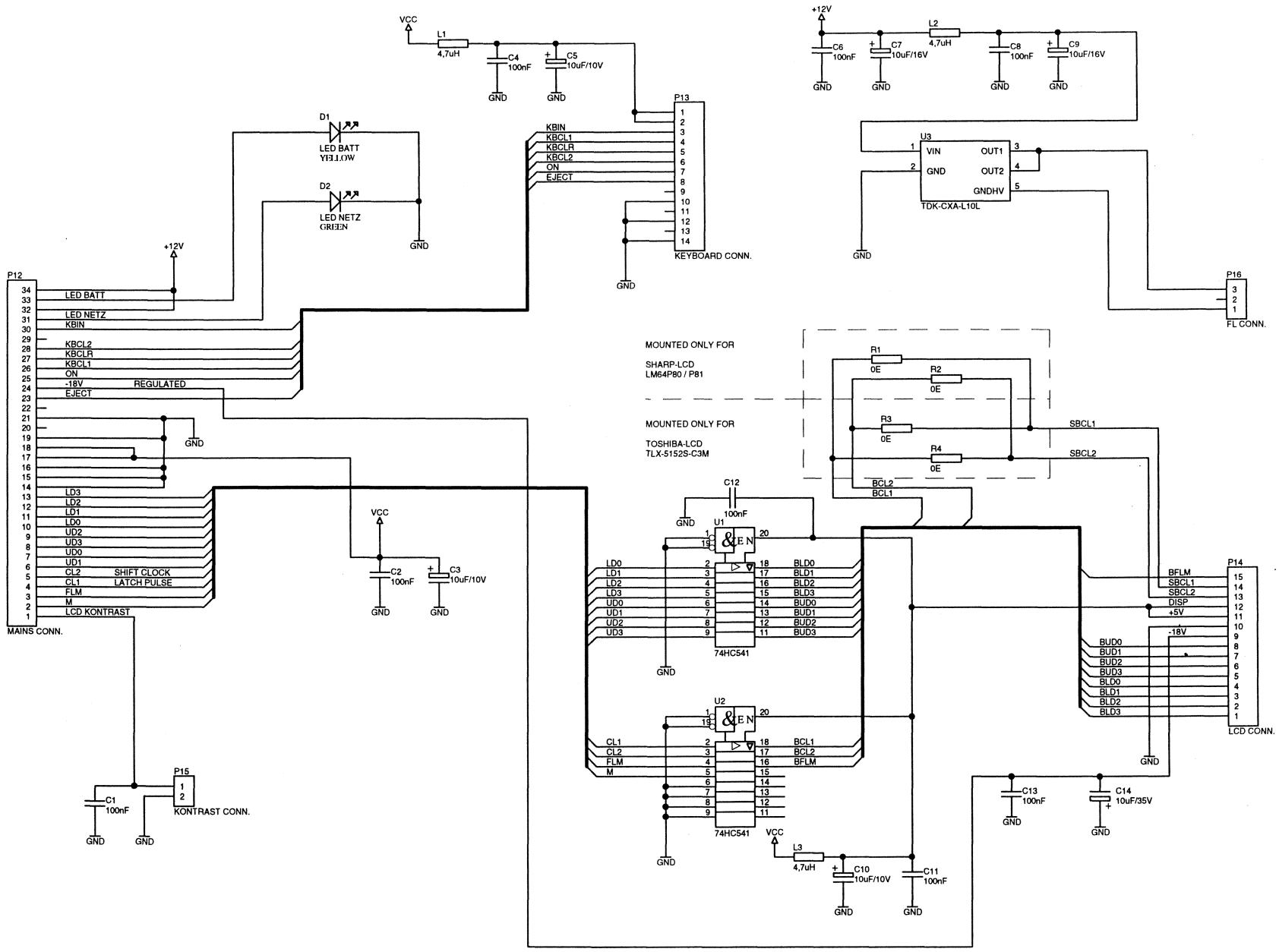
KS0/KS2: NC



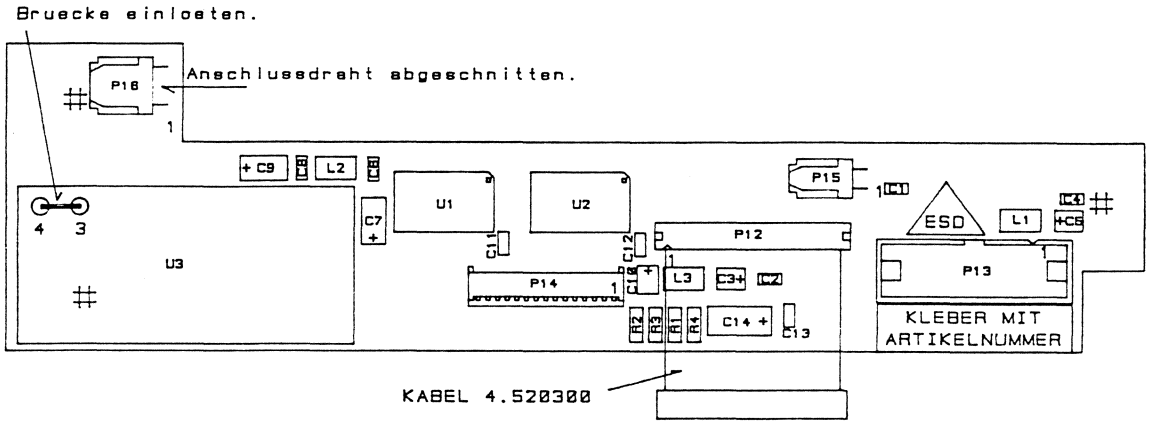
SCHILLER AG Altgasse 68 CH-6340 Baar Switzerland		
Title: KEYBOARD MK8-3 (AT-10)		
Size: B	Document Number: S.2232 Fa	REV:
Date: September 19, 1994	Sheet 3 of 3	

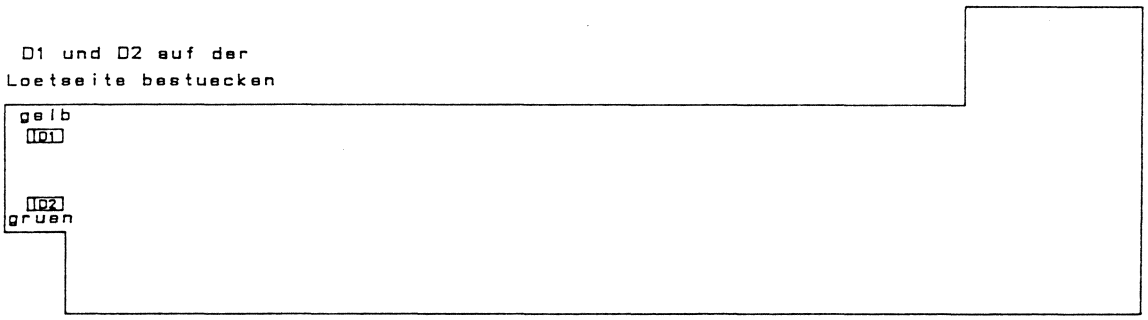
AT-10 Circuit Diagrams and Board Layouts

Connector Board MK 8-4



Title CONNECTOR-BOARD MK8-4 (AT-10) MB				Schiller AG Altgasse 68 CH-6340 Baar Switzerland
Size: A3	Nr: S.2236 Cb	Revision:		
Drawn by:Gau		Date: 24.06.96	Sheet 1 of 1	

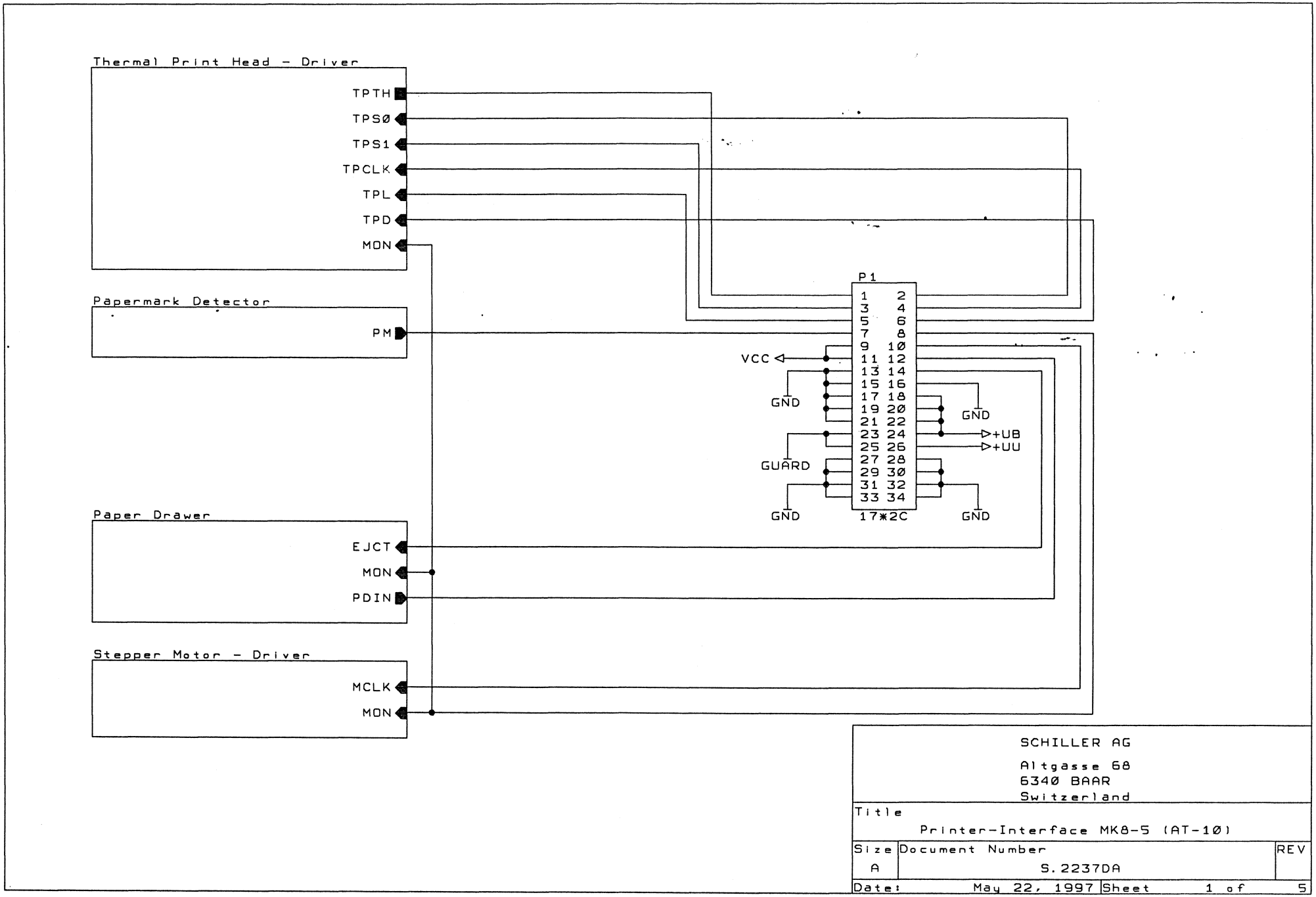




	SCHILLER AG Altgasse 68 CH-6340 Baar Switzerland	Visum Gez : Gau Entw : Prod :
Title CONNECTOR-BOARD MK8-4		AT-10
Size	Document Number D.2236Cb	
Date: 24.06.96	Sheet	1 of 1

AT-10 Circuit Diagrams and Board Layouts

Printer Interface MK 8-5



SCHILLER AG

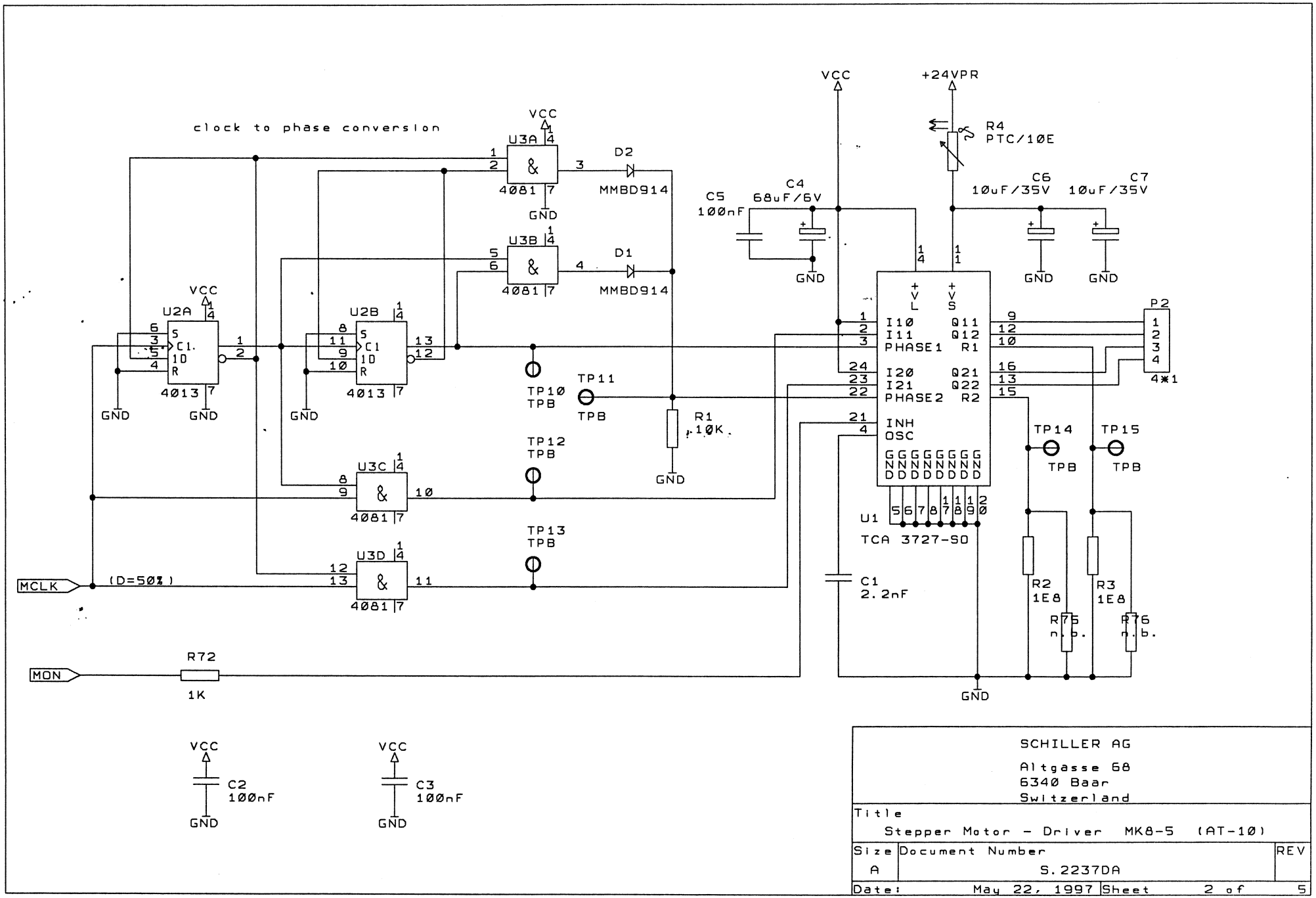
Altgasse 68
6340 BAAR
Switzerland

Title

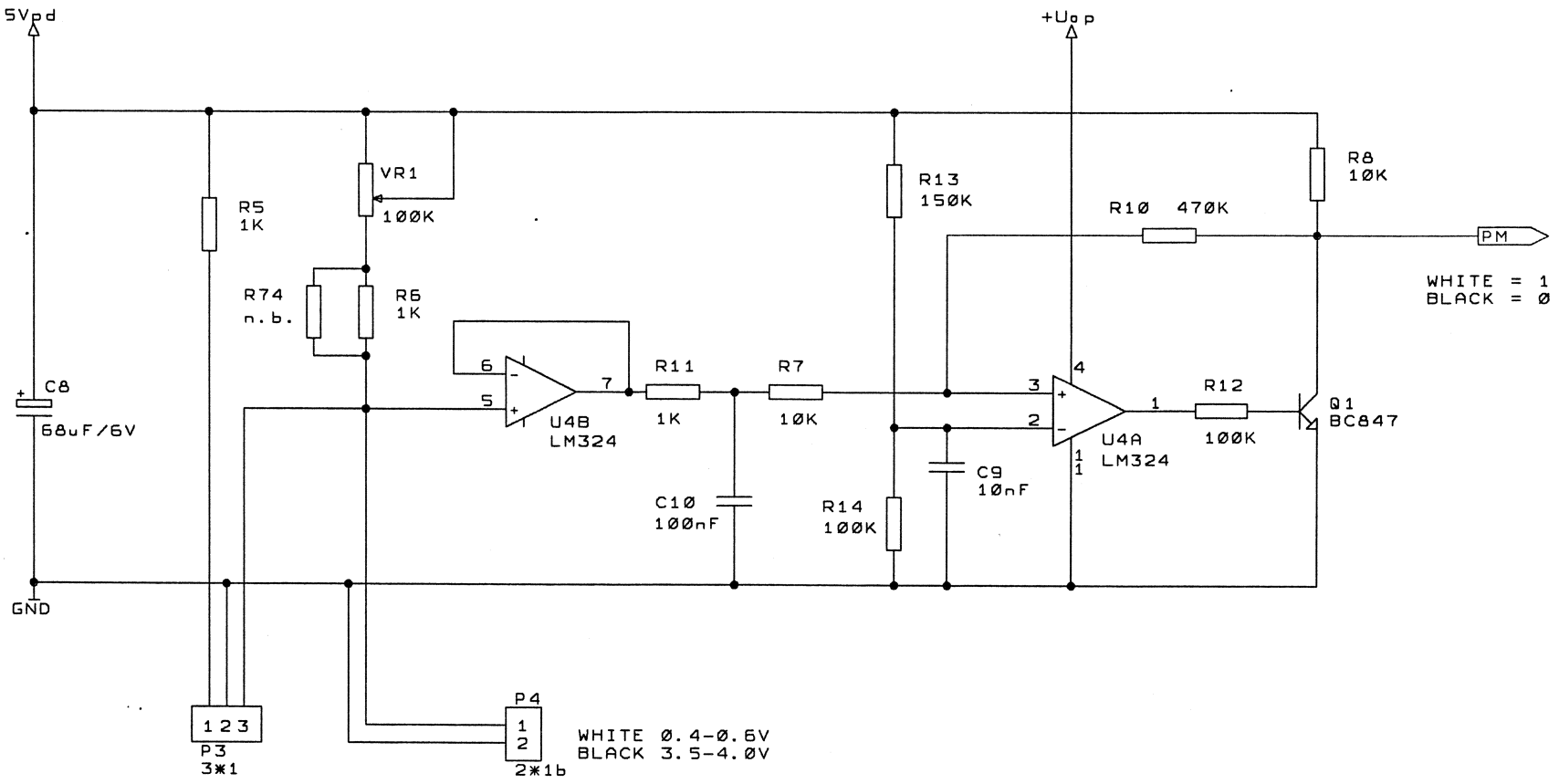
Printer-Interface MK8-5 (AT-10)

Size	Document Number	REV
A	S.2237DA	

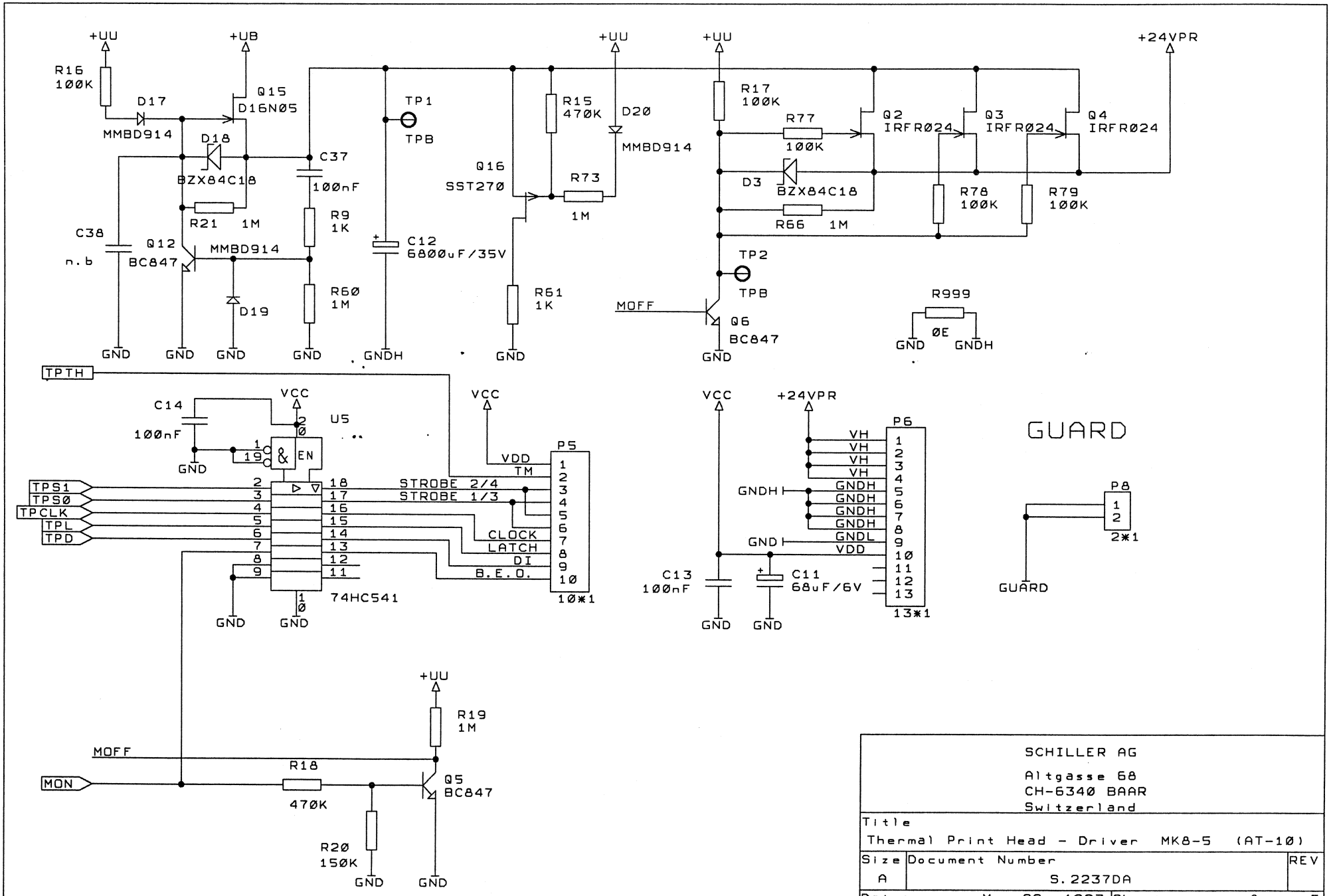
Date: May 22, 1997 Sheet 1 of 5



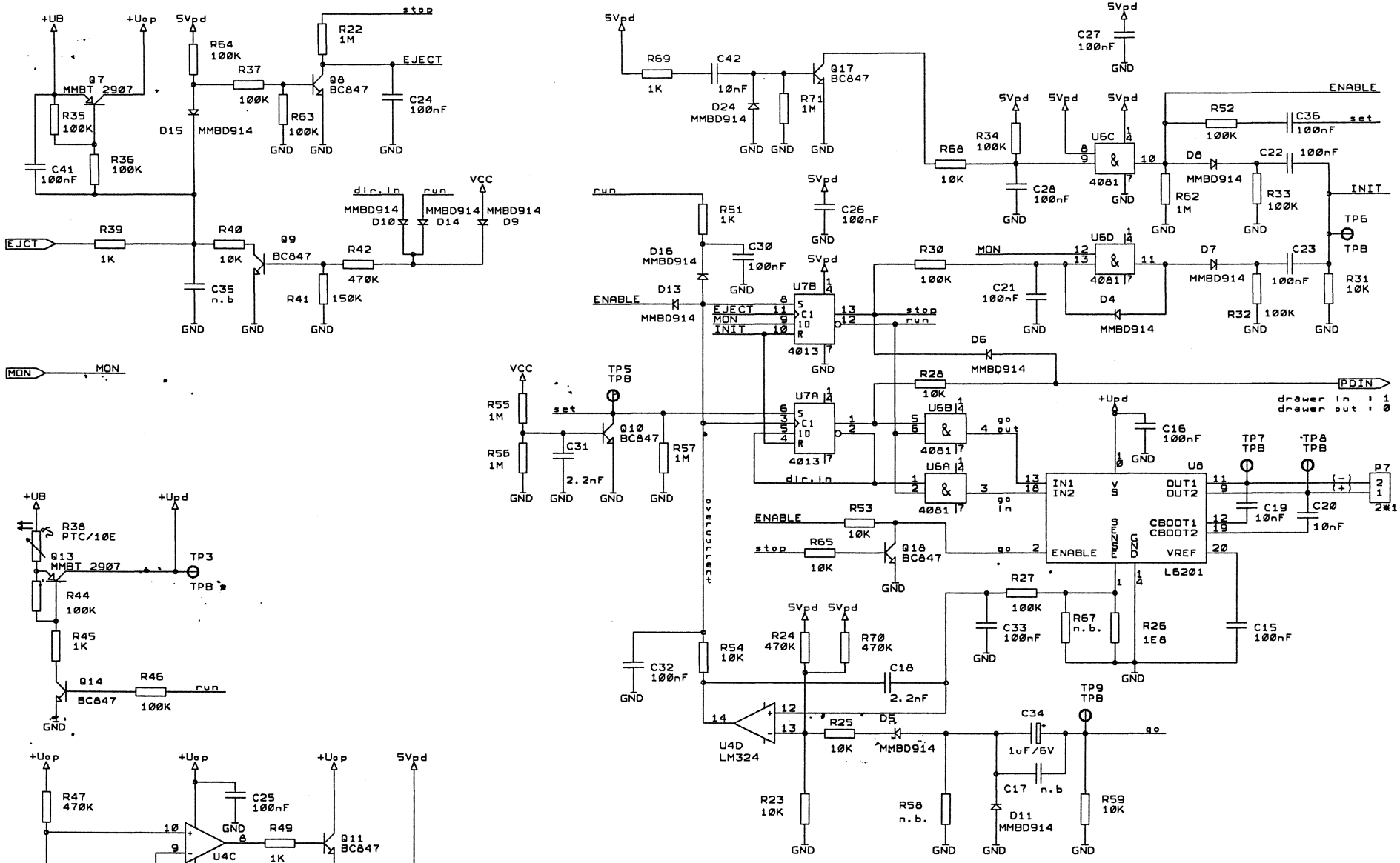
SCHILLER AG Altgasse 68 6340 Baar Switzerland	
Title Stepper Motor - Driver MK8-5 (AT-10)	
Size A	Document Number S.2237DA REV
Date: May 22, 1997	Sheet 2 of 5



SCHILLER AG Altgasse 68 CH-6340 BAAR Switzerland		
Title Papermark Detector MK8-5		
Size A	Document Number S.2237DA	REV
Date: May 22, 1997	Sheet 3 of 5	



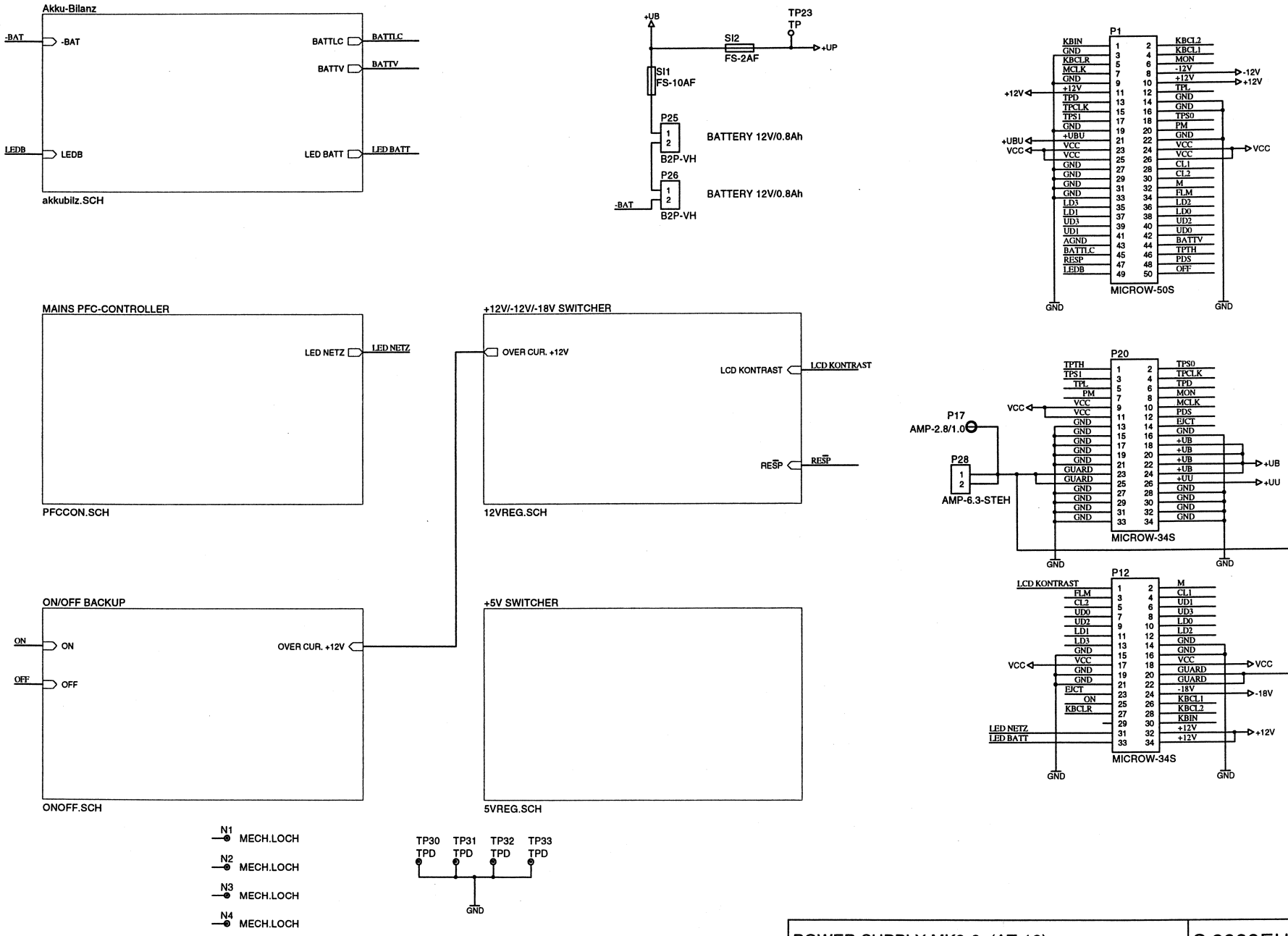
SCHILLER AG Altgasse 68 CH-6340 BAAR Switzerland	
Title: Thermal Print Head - Driver MK8-5 (AT-10)	
Size: A	Document Number: S.2237DA — REV:
Date: May 22, 1997	Sheet of 5



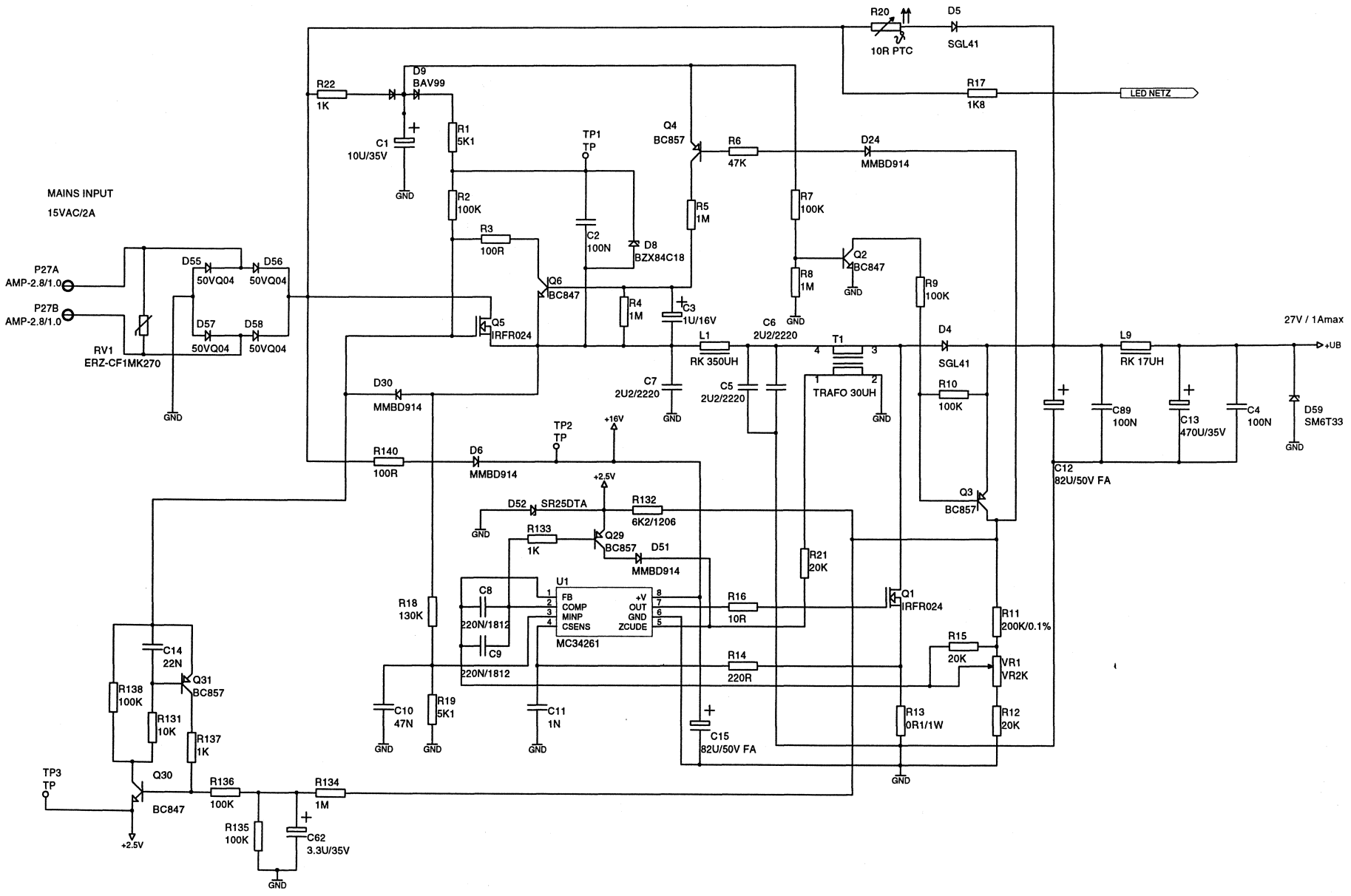
SCHILLER AG Altgasse 68 CH-6340 BAAR Switzerland		
Title: Paperdrawer MK8-5 (AT-10)		
Size: B	Document Number: S.2237DA	REV
Date: May 22, 1997	Sheet 5 of 5	

AT-10 Circuit Diagrams and Board Layouts

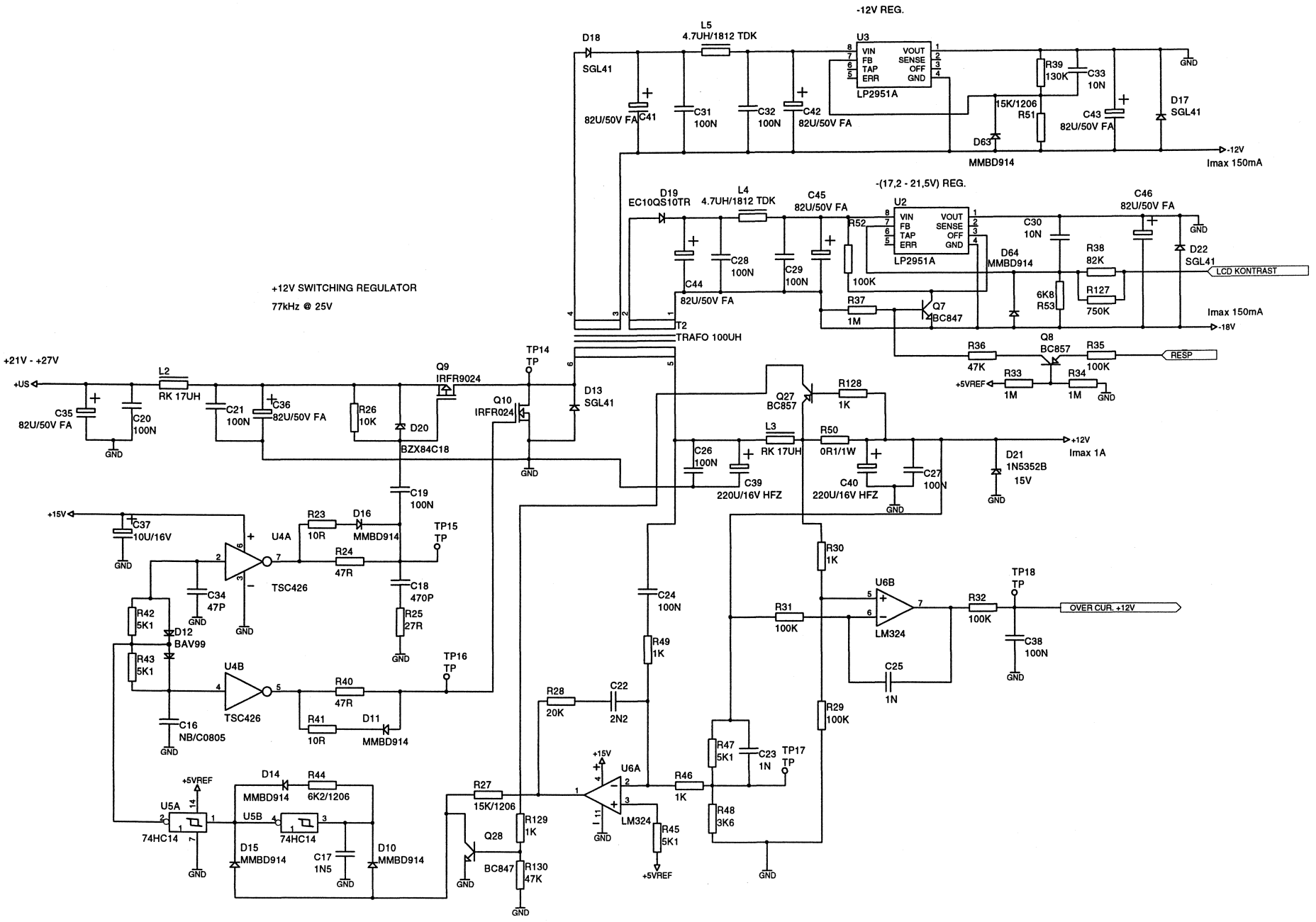
Power Supply MK 8-6



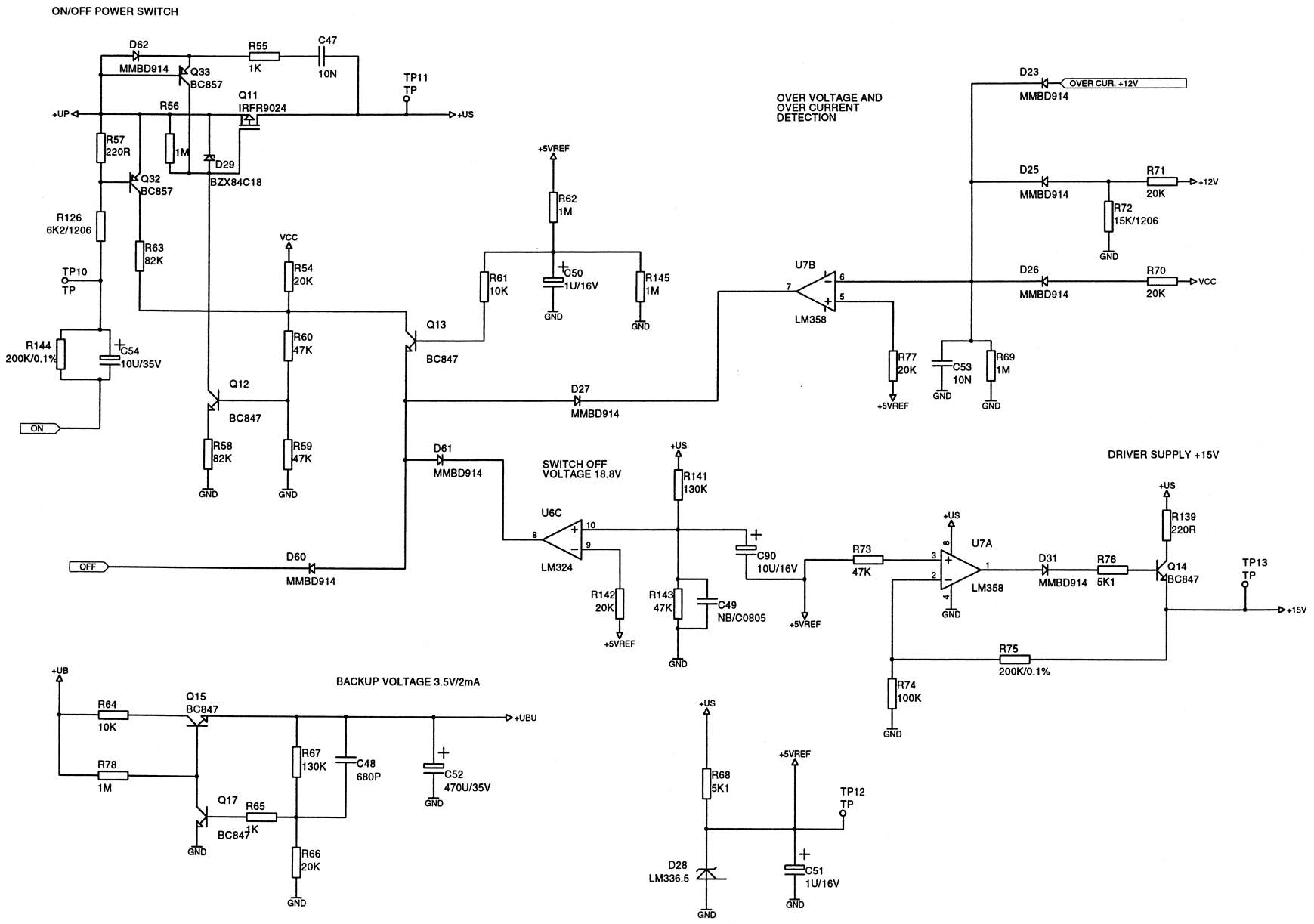
POWER SUPPLY MK8-6 (AT-10)				S.2238EH	Schiller AG Altgasse 68 CH-6340 Baar
Size: A3	Drawn by: STR	Entw: HSch	Date: 20.04.99	Sheet 1 of 6	



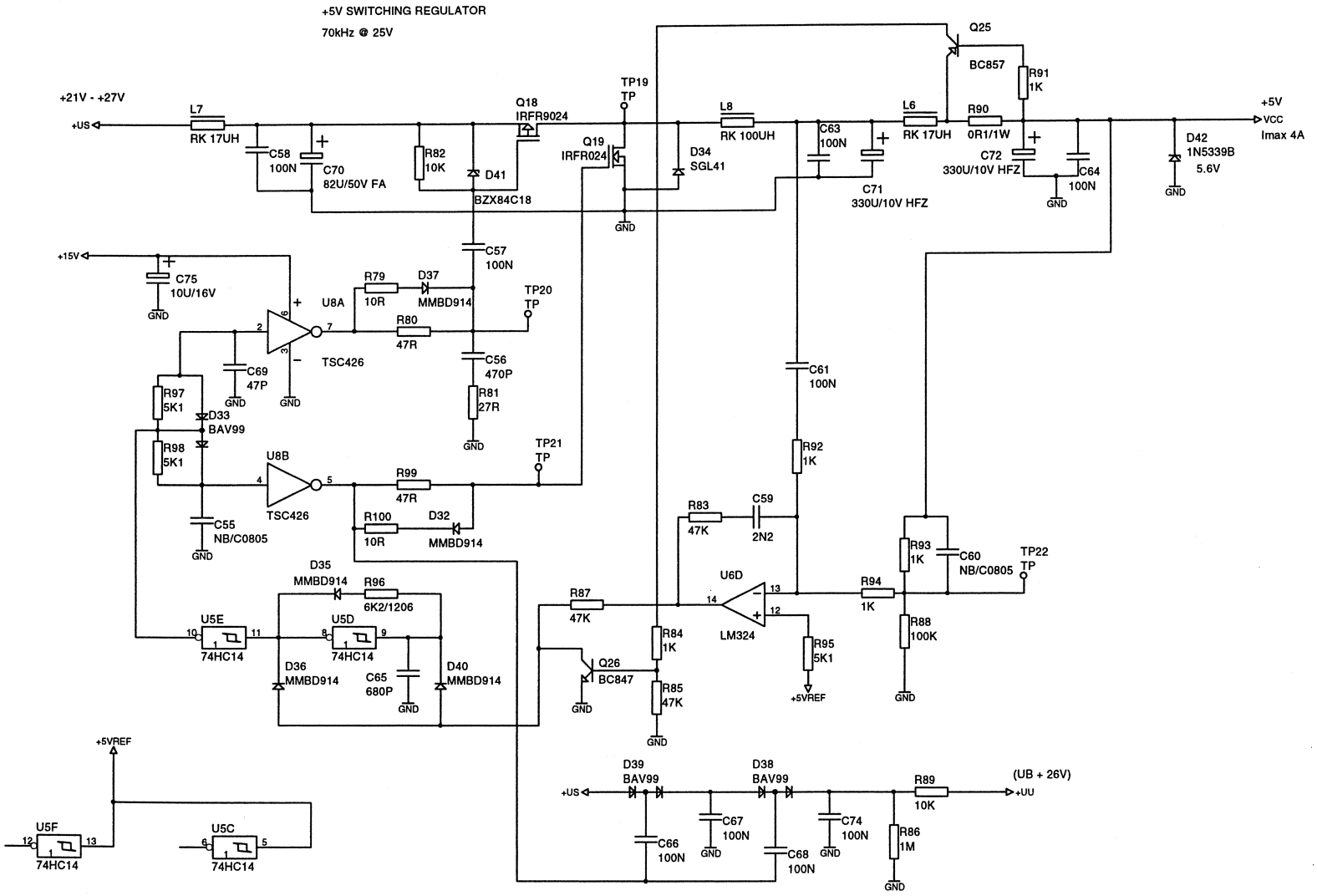
MAINS PFC-CONTROLLER MK8-6 (AT-10)				S.2238EH	Schiller AG Altgasse 68 CH-6340 Baar
Size: A3	Drawn by: STR	Entw: HSch	Date: 20.04.99	Sheet 2 of 6	



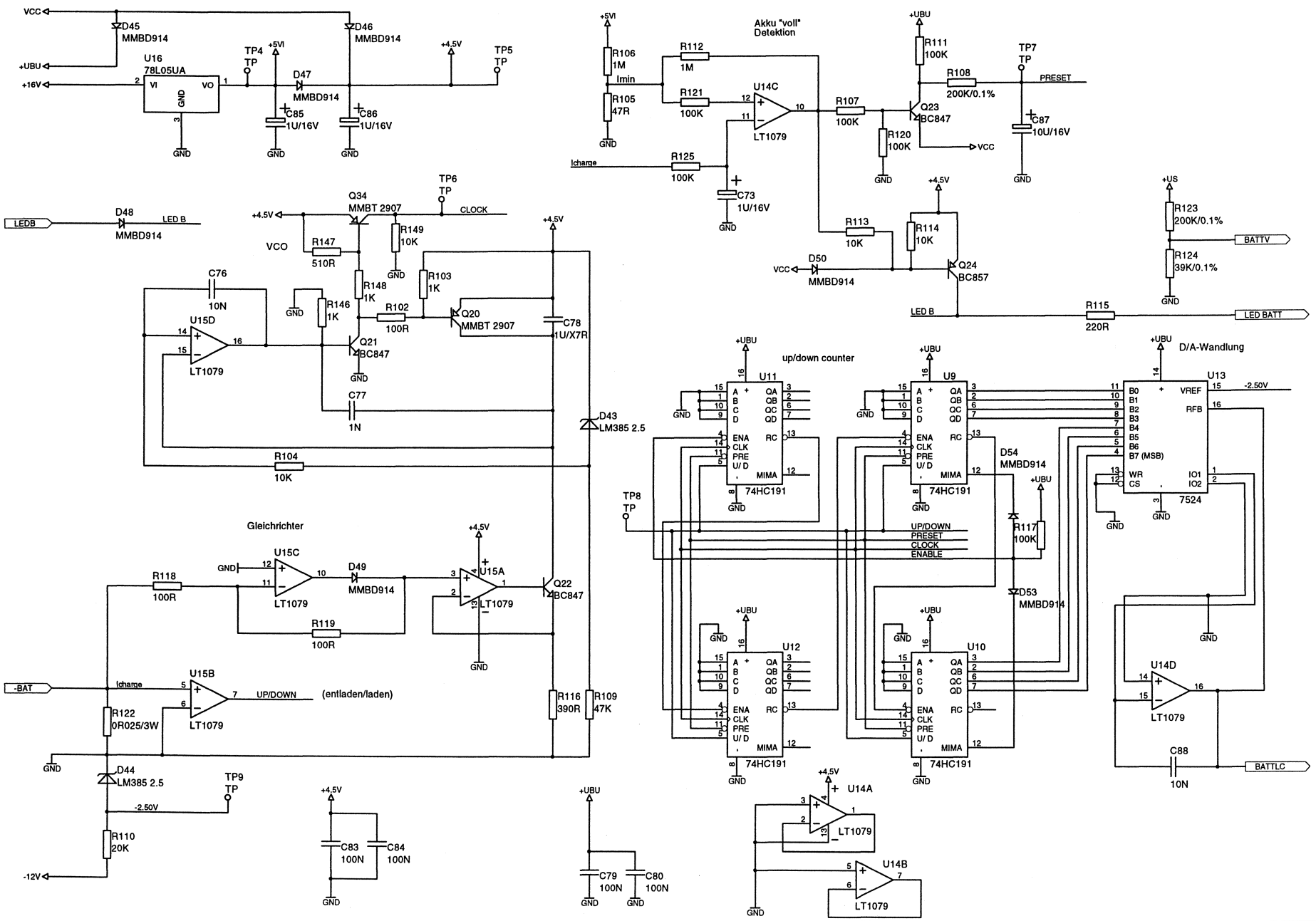
+12V/-12V/-18V SWITCHER MK8-6 (AT-10)				S.2238EH	Schiller AG Altgasse 68 CH-6340 Baar
Size: A3	Drawn by: STR	Entw: HSch	Date: 20.04.99	Sheet 3 of 6	



ON/OFF BACKUP MK8-6 (AT-10)				S.2238EH	Schiller AG Altgasse 68 CH-6340 Baar
Size: A3	Drawn by: STR	Entw: HSch	Date: 20.04.99	Sheet 4 of 6	

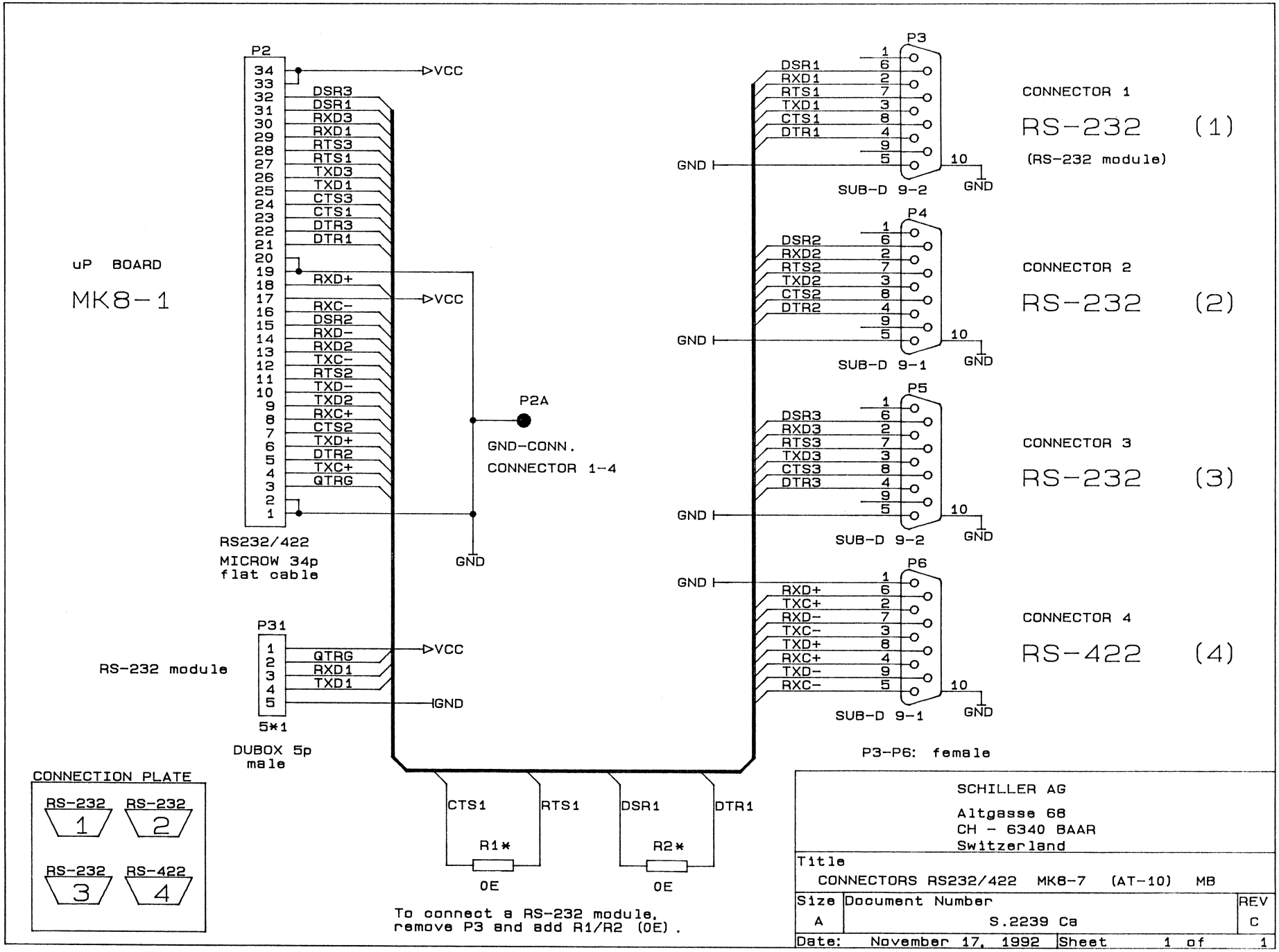


+5V SWITCHER MK8-6 (AT-10)				S.2238EH	Schiller AG Altgasse 68 CH-6340 Baar
Size: A3	Drawn by: STR	Entw: HSch	Date: 20.04.99	Sheet 5 of 6	


VCC
+UBU
+16V
D45
MMBD914
D46
MMBD914
U16
78L05UA
VI
VO
GND
TP4
TP
+5VI
D47
MMBD914
+4,5V
TP5
TP
C85
1U/16V
C86
1U/16V
GND
+5VI
R106
1M
Imin
R105
47R
R112
1M
R121
100K
U14C
LT1079
Akku "voll"
Detektion
+UBU
R111
100K
R108
200K/0.1%
TP7
TP
PRESET
R107
100K
Q23
BC847
R120
100K
VCC
C87
10U/16V
Icharge
R125
100K
C73
1U/16V
+4,5V
R113
10K
R114
10K
D50
MMBD914
Q24
BC857
+US
R123
200K/0.1%
BATTV
R124
39K/0.1%
LED B
R115
220R
LED BATT
LEDB
D48
MMBD914
LED B
TP6
TP
Q34
MMBT 2907
CLOCK
VCO
R147
510R
R149
10K
R148
1K
R103
1K
C76
10N
U15D
LT1079
R146
1K
R102
100R
Q20
MMBT 2907
Q21
BC847
C78
1U/X7R
C77
1N
D43
LM385 2.5
R104
10K
TP8
TP
up/down counter
U11
U9
74HC191
D54
MMBD914
D/A-Wandlung
U13
7524
VREF
RFB
B0
B1
B2
B3
B4
B5
B6
B7 (MSB)
WR
CS
IO1
IO2
-2.50V
UP/DOWN
PRESET
CLOCK
ENABLE
R117
100K
D53
MMBD914
Gleichrichter
U15C
LT1079
R118
100R
D49
MMBD914
U15A
Q22
BC847
R119
100R
U15B
-BAT
Icharge
UP/DOWN
(entladen/laden)
R122
0R025/3W
R116
390R
R109
47K
U12
U10
U14D
C88
10N
BATTLC
D44
LM385 2.5
TP9
TP
-2.50V
R110
20K
-12V
C83
100N
C84
100N
C79
100N
C80
100N
U14A
U14B
Akku-Bilanz MK8-6 (AT-10)
S.2238EH
Schiller AG
Altgasse 68
CH-6340 Baar
Size: A3
Drawn by: STR
Entw: HSch
Date: 20.04.99
Sheet 6 of 6

AT-10 Circuit Diagrams and Board Layouts

Connector RS-232/455 MK 8-7



SCHILLER AG
Altgasse 66
CH - 6340 BAAR
Switzerland

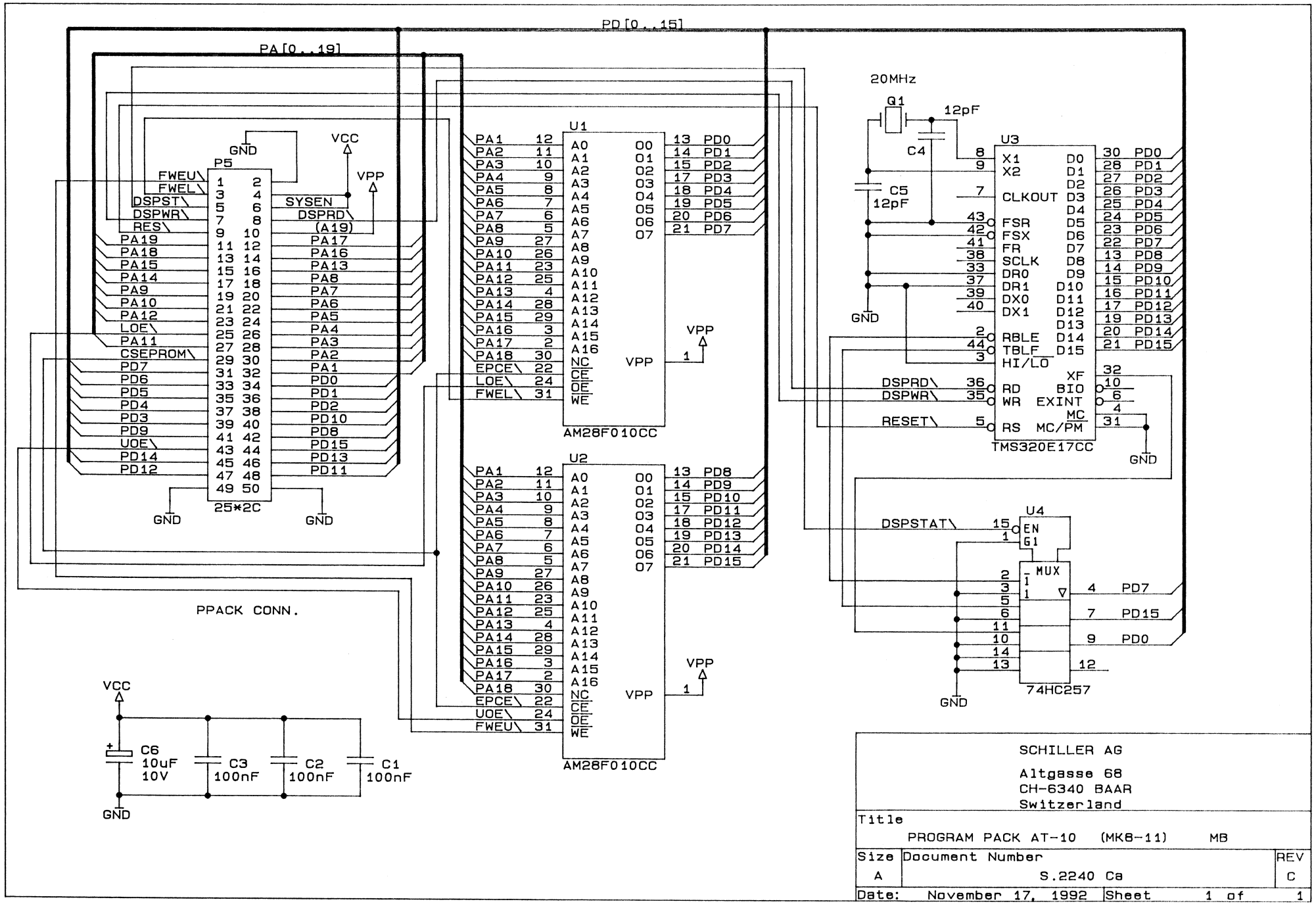
Title
CONNECTORS RS232/422 MK8-7 (AT-10) MB

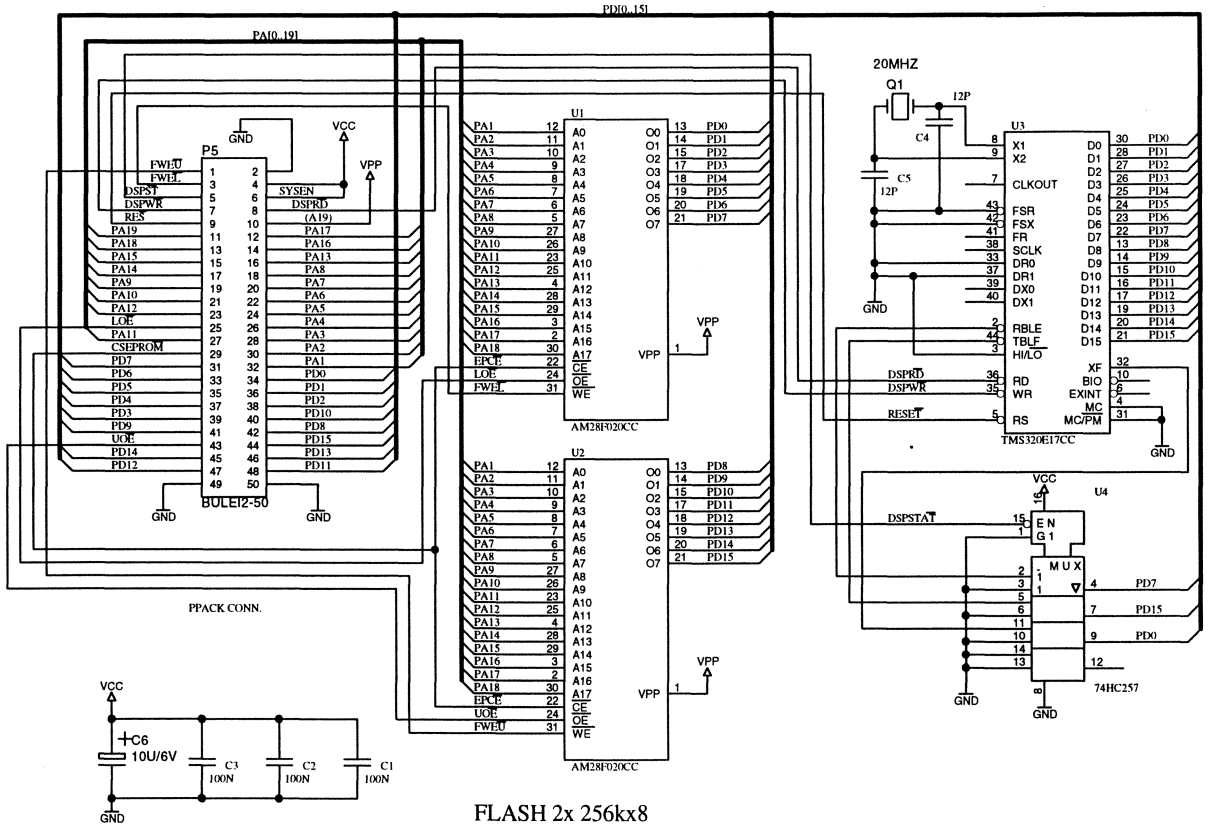
Size	Document Number	REV
A	S.2239 Ca	C

Date: November 17, 1992 Sheet 1 of 1

AT-10 Circuit Diagrams and Board Layouts

Program Pack MK 8-11


PD[0..15]
PA[0..19]
20MHz
Q1
12pF
C4
C5
12pF
U3
TMS320E17CC
U1
AM28F010CC
U2
AM28F010CC
P5
25*2C
PPACK CONN.
U4
74HC257
DSPSTAT\
DSPRD\
DSPWR\
RESET\
SYSEN
DSPRD\
(A19)
FWEU\
FWEL\
DSPST\
DSPWR\
RES\
LOE\
UOE\
CSEPROM\
EPCE\
VCC
VPP
GND
C6
10uF
10V
C3
100nF
C2
100nF
C1
100nF
SCHILLER AG
Altgasse 68
CH-6340 BAAR
Switzerland
Title
PROGRAM PACK AT-10 (MK8-11) MB
Size
A
Document Number
S.2240 Ca
REV
C
Date: November 17, 1992
Sheet 1 of 1



FLASH 2x 256kx8

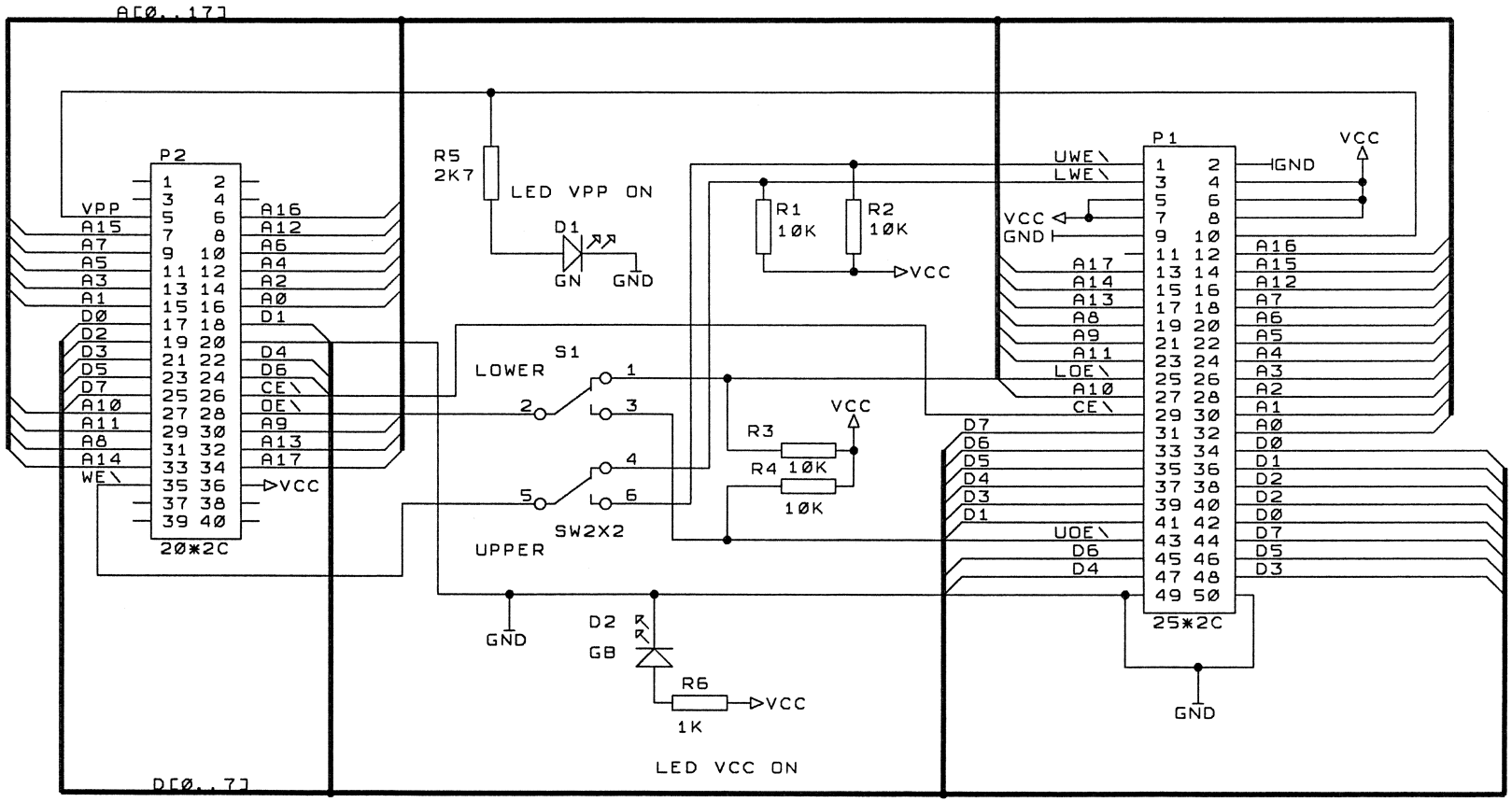
PROGRAM PACK AT-10 (MK8-11)				S.2242 Ca	Schiller AG Altgasse 68 CH-6340 Baar
Size: A3	Drawn by: Gau	Entw: MB	Date: 28.12.92	Sheet 1 of 1	

AT-10 Circuit Diagrams and Board Layouts

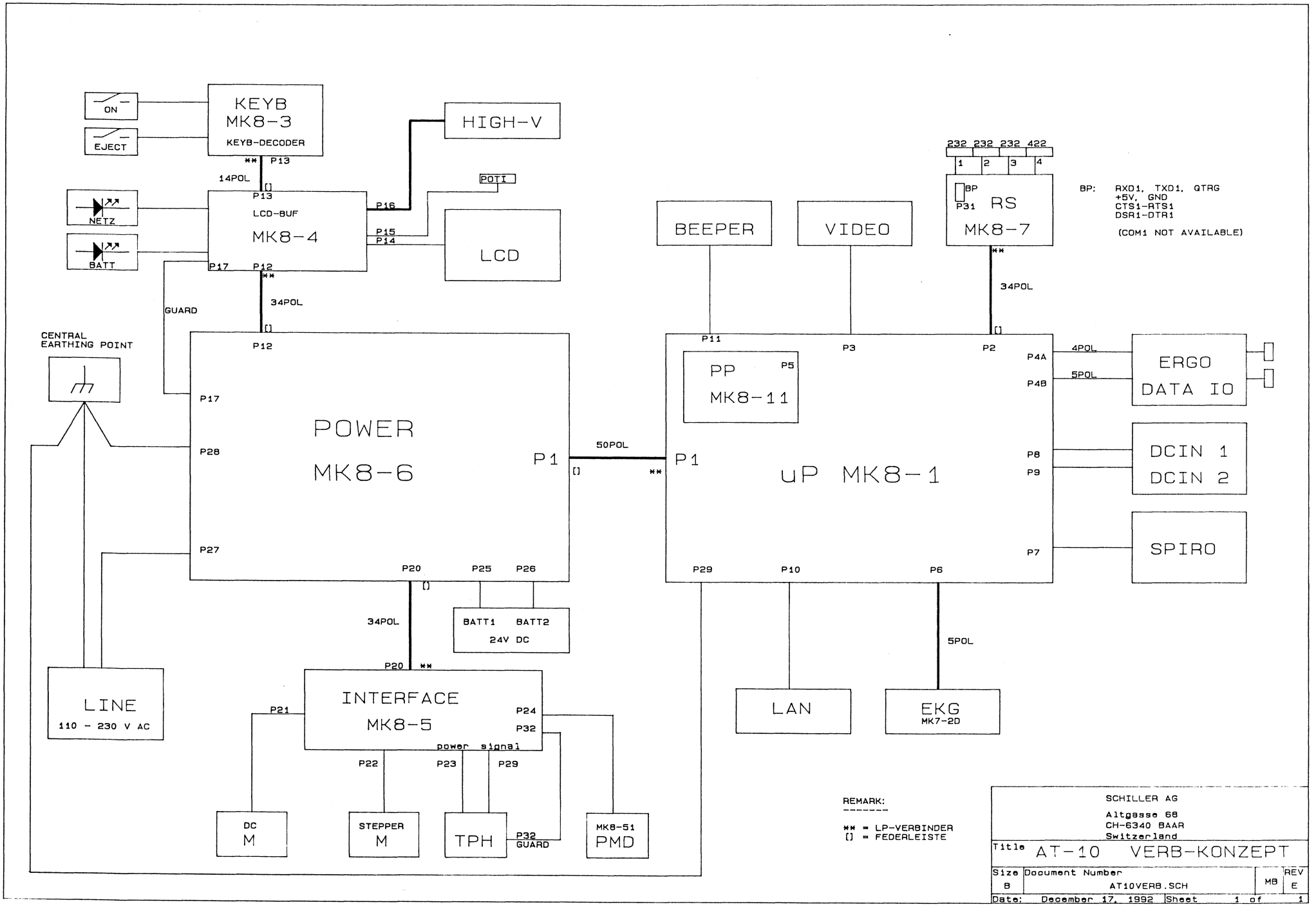
Miscellaneous

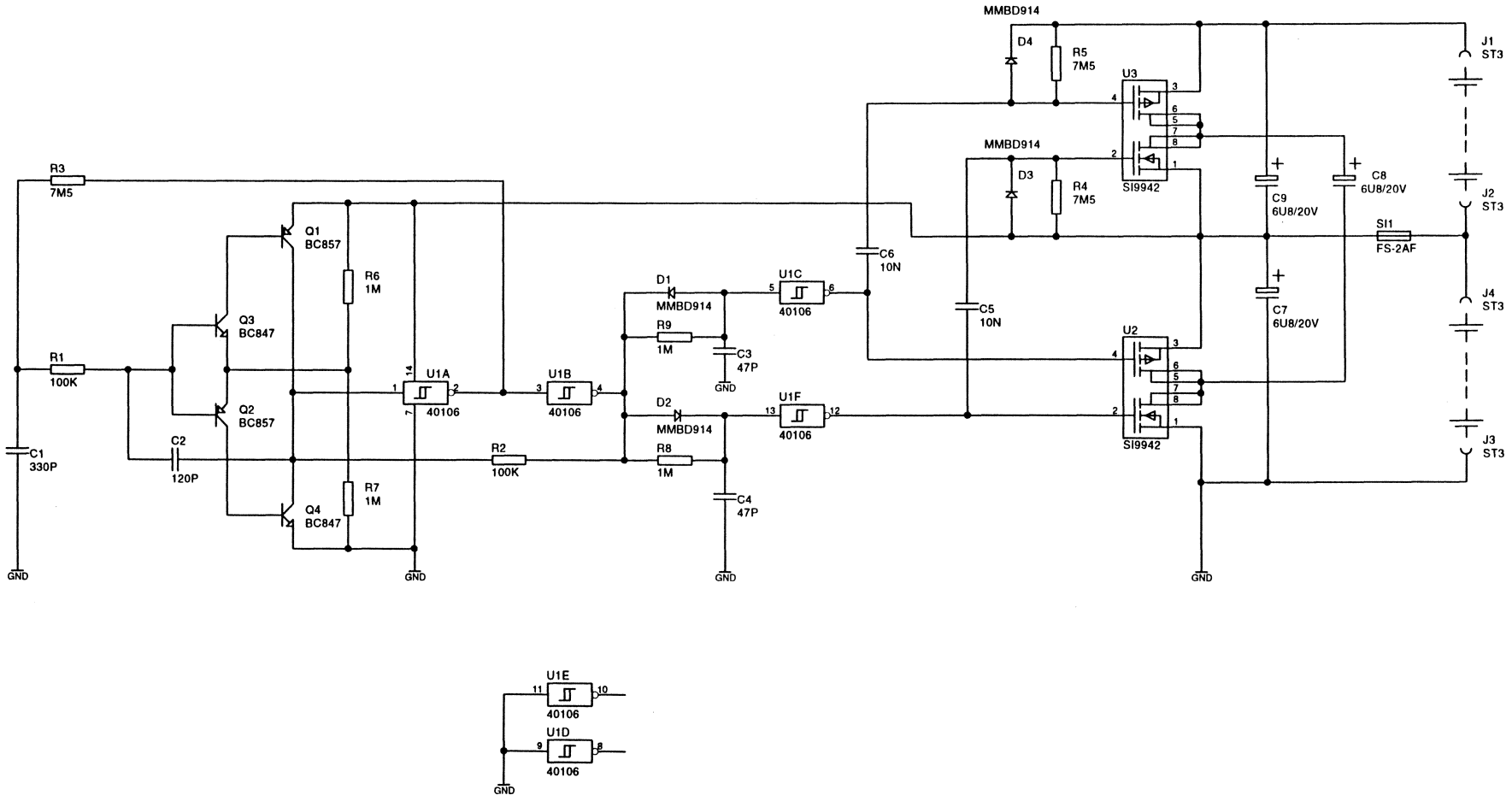
PROGRAMMER

PPACK MK8-11



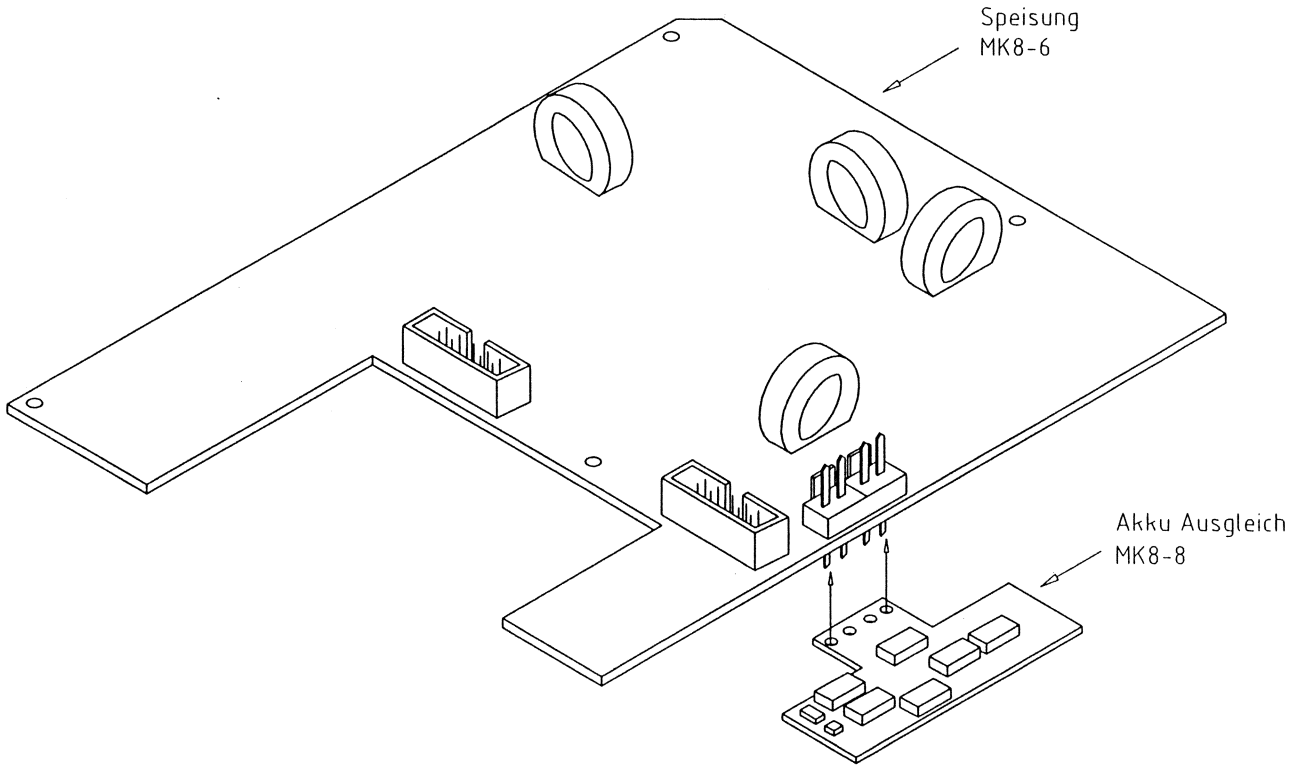
	SCHILLER AG Altgase 68 CH-6340 BAAR Switzerland	
Title	PROGRAMMER-ADAPTER AT-10 / SP-10 MB	
Size A	Document Number	REV B
Date: December 30, 1992	Sheet 1 of 1	


ON
EJECT
KEYB
MK8-3
KEYB-DECODER
HIGH-V
14POL
P13
POTI
NETZ
BATT
LCD-BUF
MK8-4
P16
P15
P14
LCD
P17
P12
34POL
GUARD
CENTRAL
EARTHING POINT
BEEPER
VIDEO
232 232 232 422
1 2 3 4
BP
P31 RS
MK8-7
BP: RXD1, TXD1, QTRG
+5V, GND
CTS1-RTS1
DSR1-DTR1
(COM1 NOT AVAILABLE)
34POL
P11
P3
P2
PP
P5
MK8-11
P4A
P4B
4POL
5POL
ERGO
DATA IO
POWER
MK8-6
P1
50POL
uP MK8-1
P8
P9
DCIN 1
DCIN 2
P7
SPIRO
P28
P27
P20
P25
P26
P29
P10
P6
BATT1 BATT2
24V DC
34POL
5POL
LINE
110 - 230 V AC
INTERFACE
MK8-5
P21
P24
P32
power signal
P22
P23
P29
LAN
EKG
MK7-2D
DC
M
STEPPER
M
TPH
P32
GUARD
MK8-51
PMD
REMARK:
** = LP-VERBINDER
[] = FEDERLEISTE
SCHILLER AG
Altgasse 68
CH-6340 BAAR
Switzerland
Title AT-10 VERB-KONZEPT
Size B
Document Number AT10VERB.SCH
MB
REV E
Date: December 17, 1992
Sheet 1 of 1



AKKU-LADUNGSAUSGLEICH MK8-8 (AT-10)				S.2245AC	Schiller AG Altgasse 68 CH-6340 Baar
Size: A3	Drawn by: STR	Entw: HSch	Date: 20.01.98	Sheet 1 of 1	

KOPIE



Pos.	Menge	Gegenstand	Werkstoff		Oberfläche	Bemerkung
					Gezeichnet: 27. August 1997	Fu/rr
					Geprüft:	
					BG-Zuordnung: AT-10	
					Ersatz für:	Anzahl Blatt / Blatt-Nr
Index	Aenderung		Datum	Sig	File: AT10\FMK8-6	A4

SCHILLER AG
CH-6340 BAAR

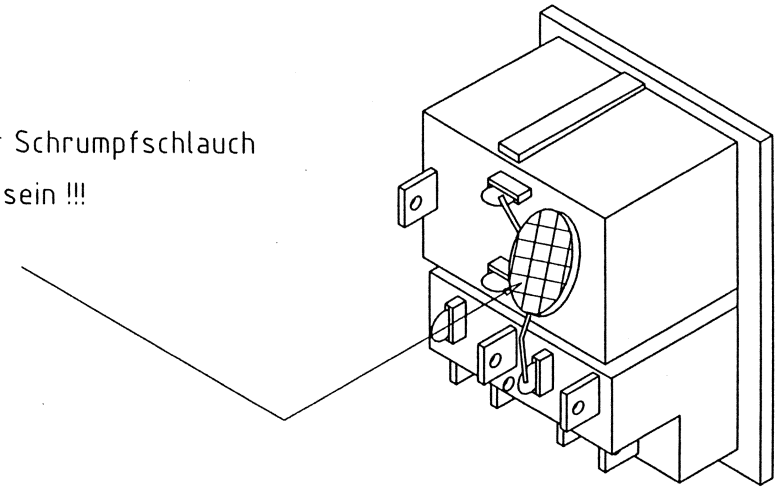
Titel: AKKU Ausgleich-Speisung

Massstab

Zeichnungs-Nr: F.MK8-6

KOPIE

Bauteil muss mit Schrumpfschlauch
ganz abgedeckt sein !!!



Pos.	Menge	Gegenstand	Werkstoff		Oberfläche		Bemerkung
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					Geprüft	27.11.96	MRü
					BG-Zuordnung	~~AT-5~~ / AT-10 / AT-60	
					Ersatz für		Anzahl Blatt
Index	Aenderung		Datum	Sig.	File: AT5\f270004	A4	Blatt-Nr.

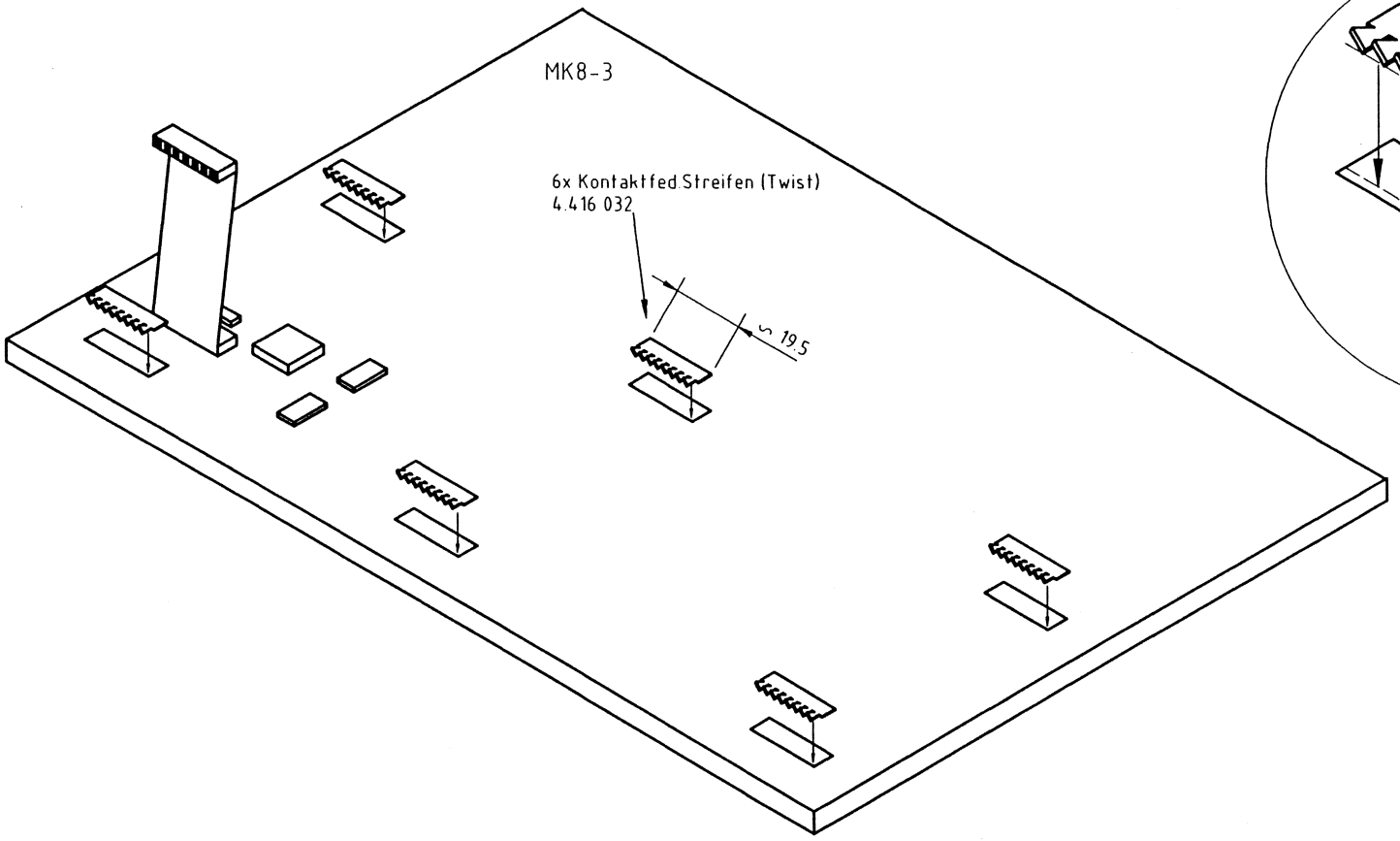
Massstab

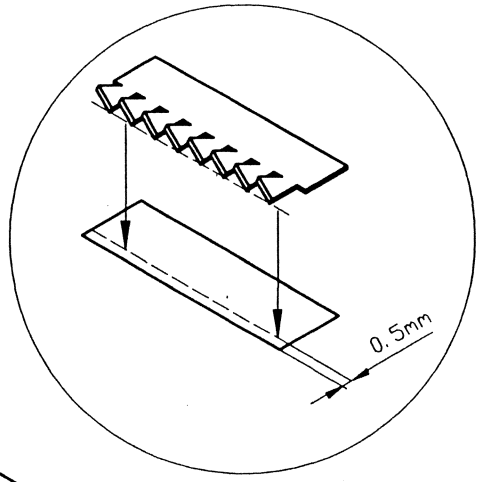
SCHILLER AG
CH-6340 BAAR

Titel: Netzbuchse

Zeichnungs-Nr.: F.270 004

KOPIE

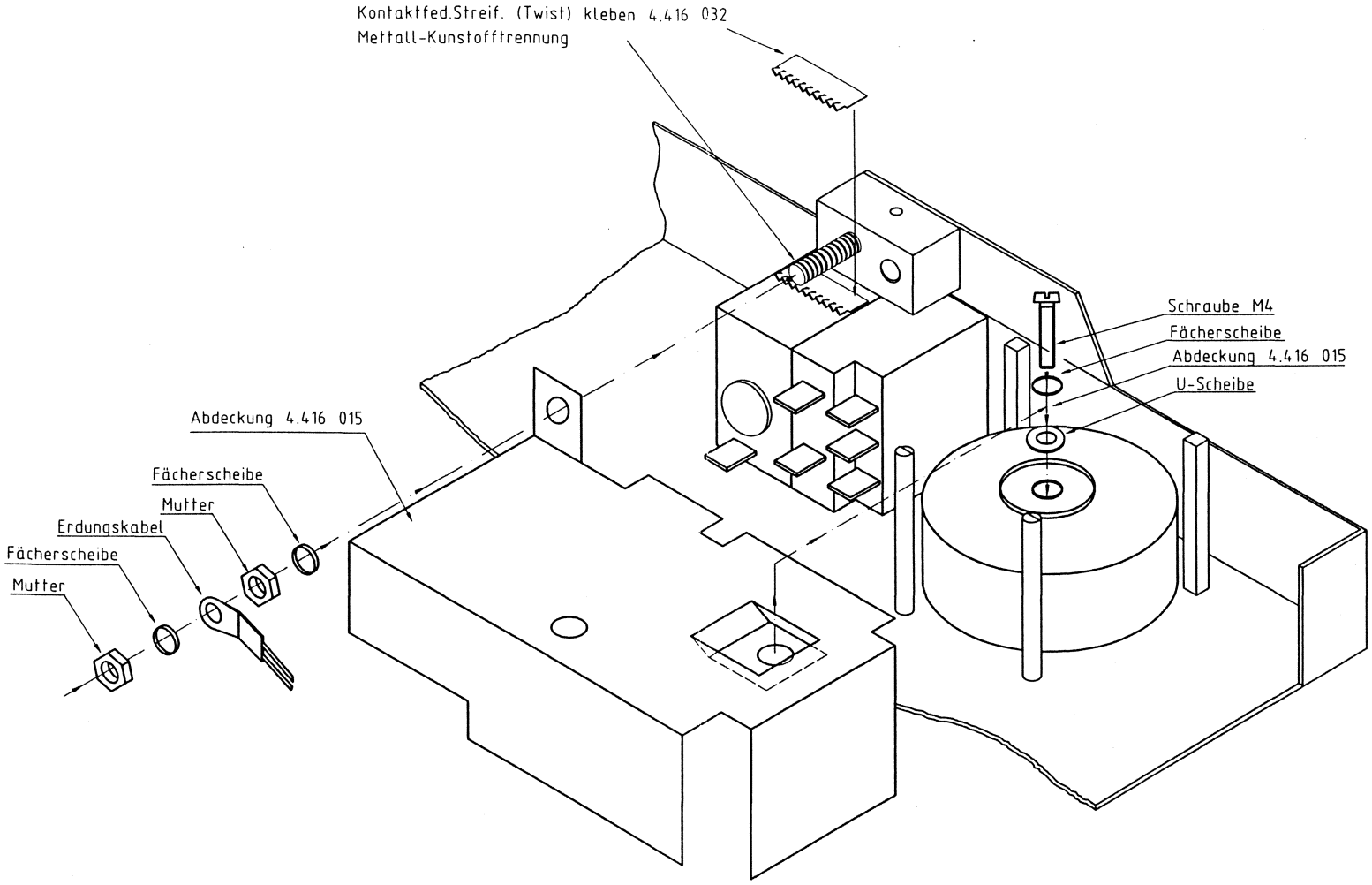




Pos.	Menge	Gegenstand	Werkstoff		Oberfl?che	Bemerkung
					Gezeichnet 16. Dezember 1994	IF/rr Massstab
					Geprüft 28.10.96	A.Ab.
					BG-Zuordnung AT-10	
a	Kontaktstr. 0.5mm von dem Rand entfernt bef		24.10.96	HSCH/rr	Ersatz f?r	Anzahl Blatt Blatt-Nr.
Index	Aenderung		Datum	Sig.	File: AT10\FMK8-3 A4	

SCHILLER AG CH-6340 BAAR	Titel Mont.von Kontaktfed.Streif.	Zeichnungs-Nr. F.MK8-3a

KOPIE



Pos.	Menge	Gegenstand	Werkstoff		Oberfläche		Bemerkung	
					Gezeichnet	23. Mai 1995	RB	Massstab
					Geprüft	25.5.95	KBy	
					BG-Zuordnung	AT-10		
					Ersatz für		Anzahl Blatt	Blatt-Nr.
Index	Aenderung		Datum	Sig.	File: AT10\F416015	A3		

SCHILLER AG CH-6340 BAAR	Titel Mont. Trafo Abdeckung	Zeichnungs-Nr. F.416 015